

Silicon-Tungsten Calorimetry

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Silicon Sensors

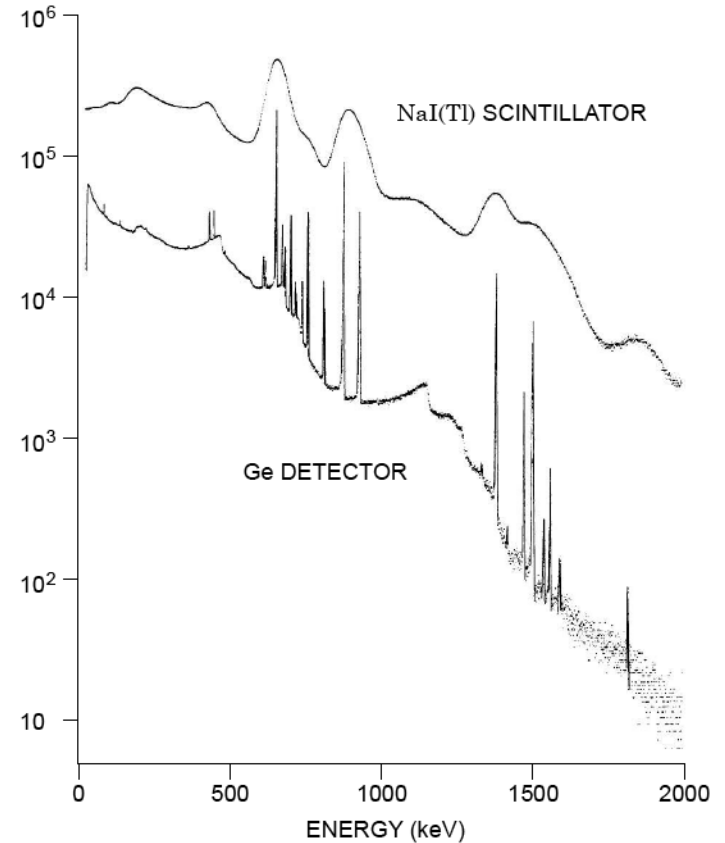
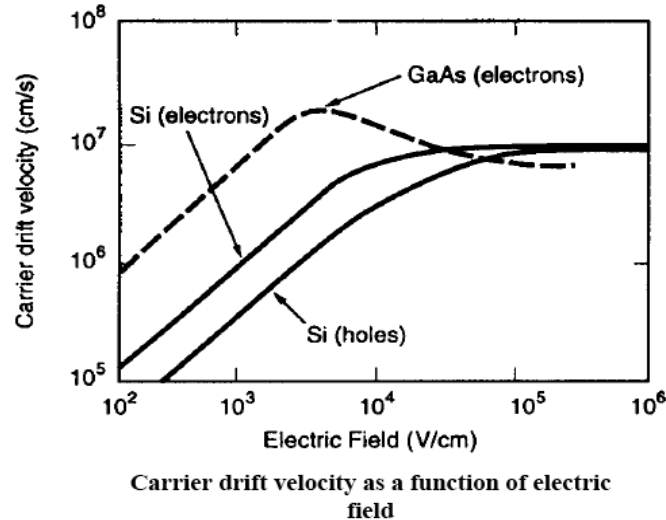
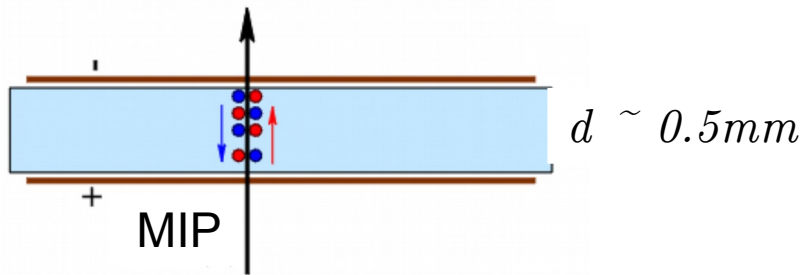
Semi-Conductor Sensors

Many types

- Si, Ge, GaAs, ...
(Diamond, Sapphire)

Principle

- Collection of ionisation charges by a high electric field
 - Drift $V_d = E\mu$, μ = mobility
 - A few 100's V



J.Cl. Philippot, IEEE Trans. Nucl. Sci. NS-17/3 (1970) 446

Carrier Statistics

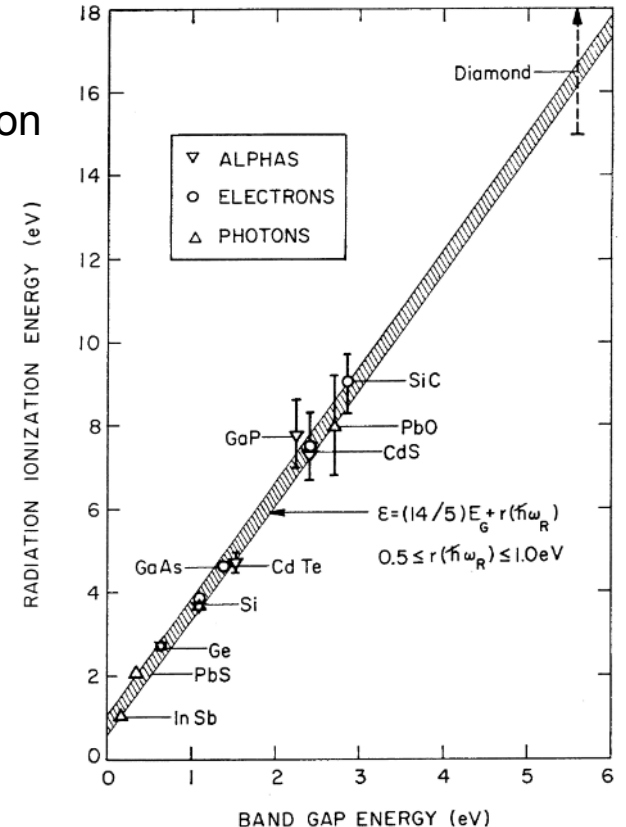
Sensitivity

- **Very high sensitivity:**
 - Many information carrier:
 - $N = E/w$ mean energy to create 1 e-h pair $w_{e-h} \sim 3.6 \text{ eV} \rightarrow \sim 100 \text{ e-h}/\mu\text{m}$ (Silicon)
- **Fano Factor = 0.12** due to binomial statistics $\sigma(E)/E = 2.35 \Delta N/N = 2.35\sqrt{Fw/E}$
Resolution $\times 3$ wrt raw Poissonian fluctuations
- **Important for**
 - Energy measurement
 - Signal-to-background ratio (trigger), data reduction, power dissipation

$$E_{\text{ionisation}} \sim 3 \times E_{\text{gap}}$$

- E+p conservation
- phonons

Ionization in gases $\sim 30 \text{ eV}$
 Ionization in semiconductors $1 - 5 \text{ eV}$
 Scintillation $\sim 10 - 1000 \text{ eV}$
 Phonons meV
 Breakup of Cooper Pairs meV



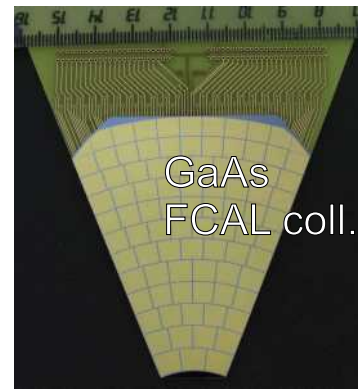
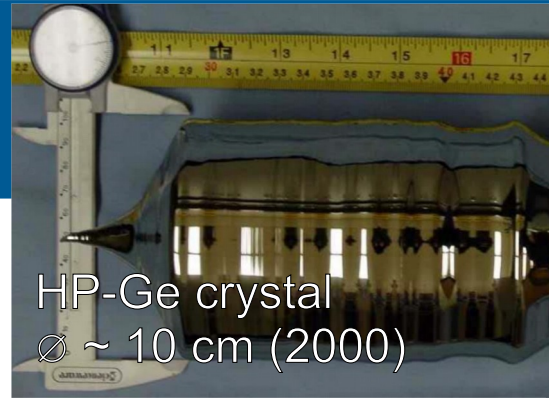
A. Klein, J. Applied Physics 39 (1968) 2029

Some candidates

Material	Z	Bandgap [eV]	Mobility [cm ² /Vs]		Density g/cm ³
			electrons	holes	
Si	14	1.1	1350	480	2.3
Ge	32	0.7	3800	1800	5.3
Diamond	6	5.5	1800	1200	3.5
GaAs	31-33	1.5	8600	400	5.4
AlSb	13-51	1.6	200	700	4.3
GaSe	31-34	2.0	60	250	4.6
CdSe	48-34	1.7	50	50	
CdS	48-16	2.4	300	15	4.8
InP	49-15	1.4	4800	150	
ZnTe	30-52	2.3	350	110	
WSe ₂	74-34	1.4	100	80	
BiI ₃	83-53	1.7	680	20	
Bi ₂ S ₃	83-16	1.3	1100	200	6.7
Cs ₃ Sb	55-51	1.6	500	10	
PbI ₂	82-53	2.6	8	2	6.2
Hgl ₂	89-53	2.1	100	4	6.3
CdTe	48-52	1.5	1100	100	6.1
CdZnTe	48-30-52	1.5-2.4			

For layers

- Small dead Space



Pros & Cons of Semi-Cond in calorimeters

High Signal ($\sim \times 10$ wrt gaseous det for same deposit)

High Charge collection (HV)

- Insensitive to magnetic field

Intrinsic Stability

Fast O(1–10ns)

Granularity O(1–100 μm)

- High Precision

Low resistivity fine for Calo's (less expensive)

Large support from industry for Silicon

- Processes, R&D

Cost

- with high variations

Fragility

Radiation damages

- In some cases

No intrinsic amplification

- Low noise readout electronics needed

Deposited Energy for electrons in Si

For minimum ionising particle ($\gamma \sim 3.5$)

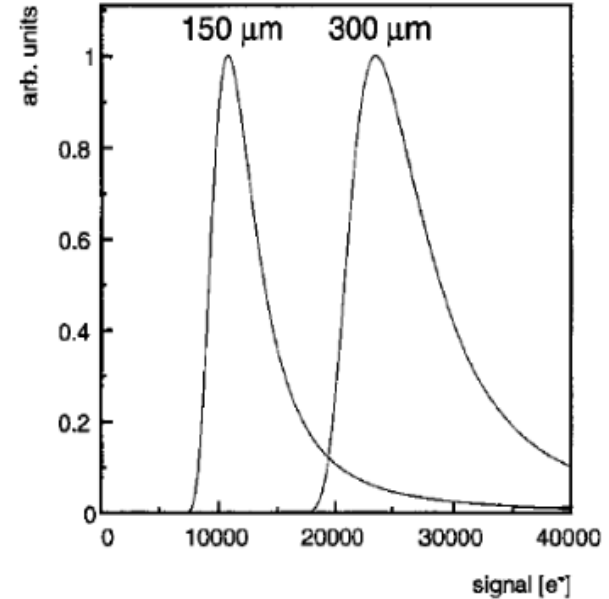
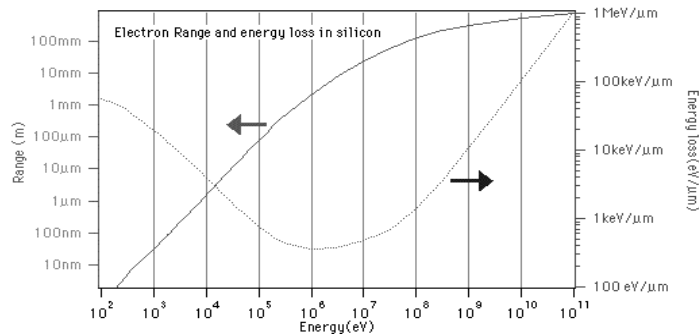
$$dE/dx = 39 \text{ keV}/100 \mu\text{m}$$

$$N_{e-h} = E_{\text{dep}}/3.6\text{eV} = \sim 30,000 \text{ e-h pairs for } 300 \mu\text{m}$$

(100/ μm , 80 in peak)

- Landau stat $N_{\text{peak}} \sim 24,000 \text{ e-h pairs} \sim 4 \text{ fC};$
[free charges $\sim 4.5 \times 10^8$]

\Rightarrow depletion & amplification needed



Landau distribution (calculated) for a 300 μm and a 150 μm thick silicon detector.

Charge carriers

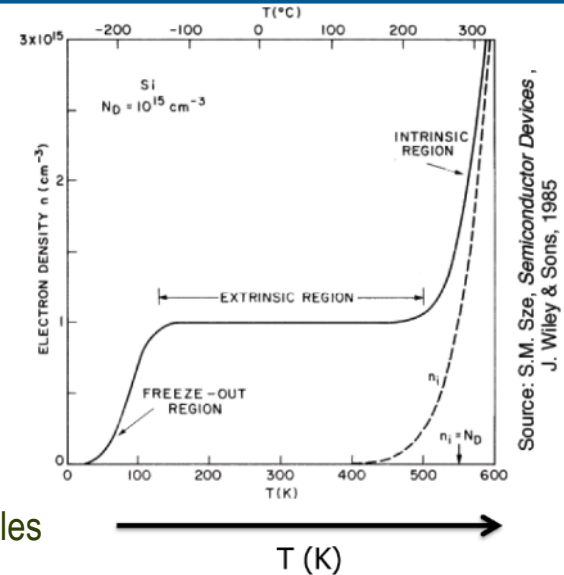
Intrinsic

- $n = p = n_i$
- $E_f = E_i = (E_c + E_v)/2 + kT \ln(N_v/N_c)$
 - Fermi level close to middle of Valence-Conduction gap.
 - $kT = 0.026\text{eV}$ @ room temp.
 - $n_i \sim T^{3/2} \exp(-E_g / 2kT) \sim 1.5 \cdot 10^{10}$ for Si (10^{-12} atoms ionised)
 - Current density $\rightarrow \rho = 230\text{k}\Omega \text{ cm}$

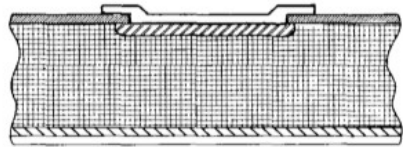
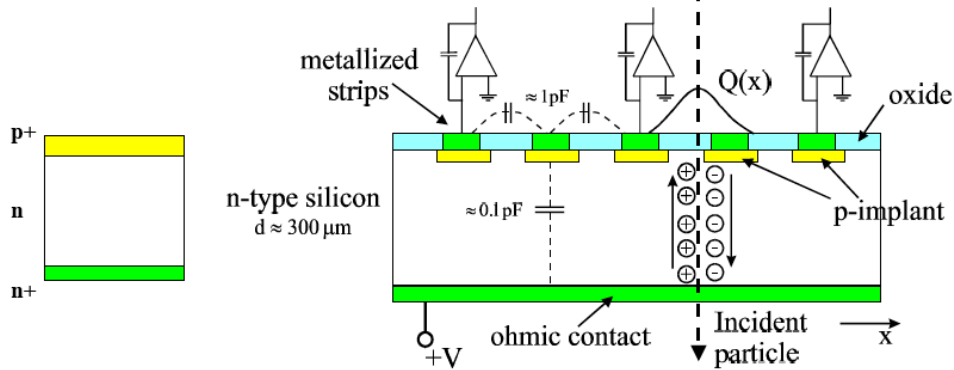
$$n_i = \sqrt{N_C N_V} \cdot \exp\left(-\frac{E_g}{2kT}\right) \propto T^{\frac{3}{2}} \cdot \exp\left(-\frac{E_g}{2kT}\right)$$

Doped

- $n = P$, As (tri-valent)
- $p = B$ (pentavalent)
- Concentrations
 - $10^{12} - 10^{18} / \text{cm}^3 \gg n_i$
- Effects:
 - changes free charges / holes
 - Diffusion length, resistivity
 - Mobility $v = \mu E$
 - Stabilize working temperature



PIN diode (reversed)



- Silicon dioxide
- n⁺ phosphorus ion implanted layer
- p⁺ boron ion implanted layer
- n⁻ high resistivity silicon
- aluminum metallizations

Fig. 12. Full p-i-n structure of a silicon detector.

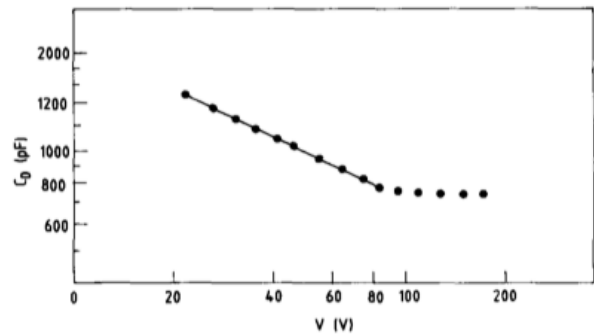
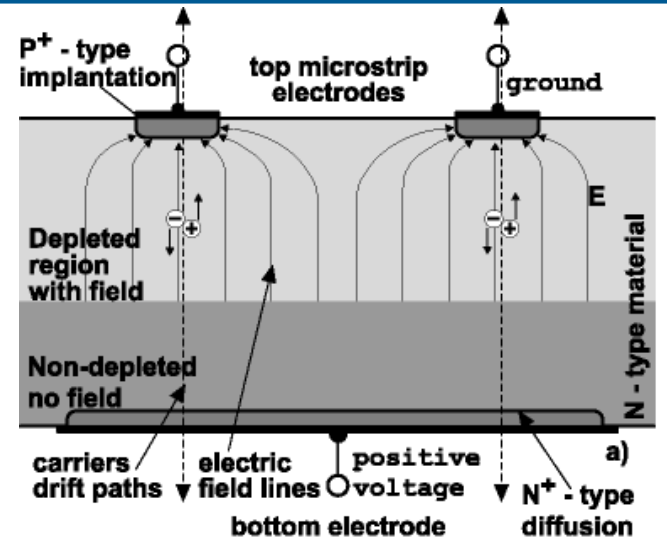
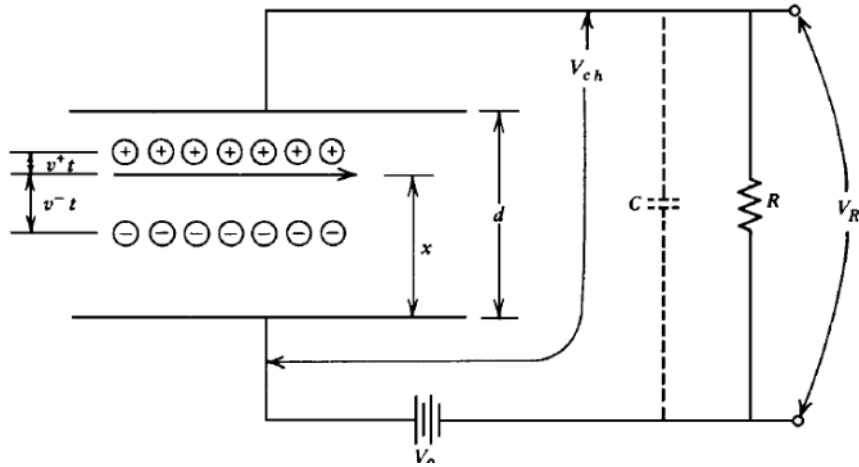


Fig. 14. Detector capacitance vs reverse bias voltage for a 28 cm² trapezoidal detector. The continuous line represents the best fit of eq. (4) to the data.

$$W \approx \left[\frac{2\epsilon_r\epsilon_0}{q} \left(\frac{N_A + N_D}{N_A N_D} \right) (V_{bi} - V) \right]^{\frac{1}{2}}$$

Depleted thickness

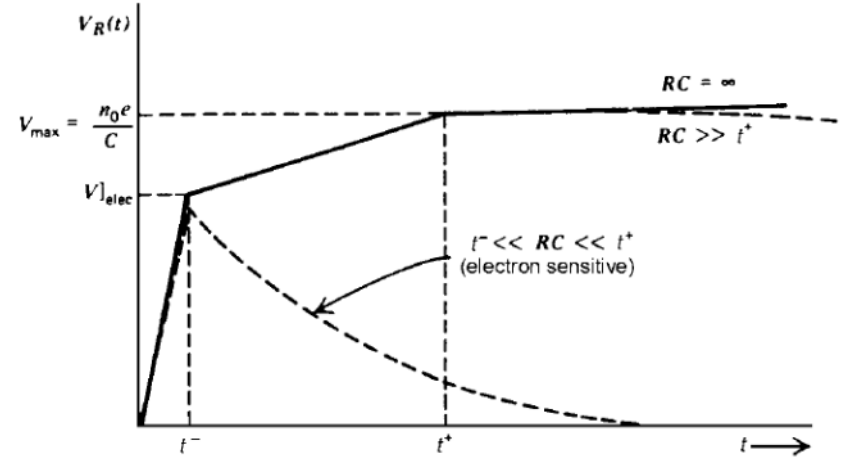
Signal shape



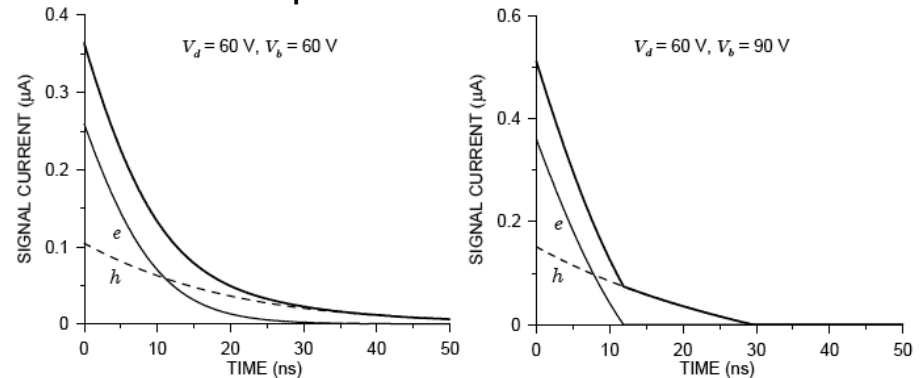
Current induced by charge movement

$$U = \frac{1}{2} CV^2$$

$$i(x) = \frac{q}{V_0} E(x)v(x),$$



300 μm thick silicon detector



Noise

Expressed in ENC (Equivalent Noise Charge)

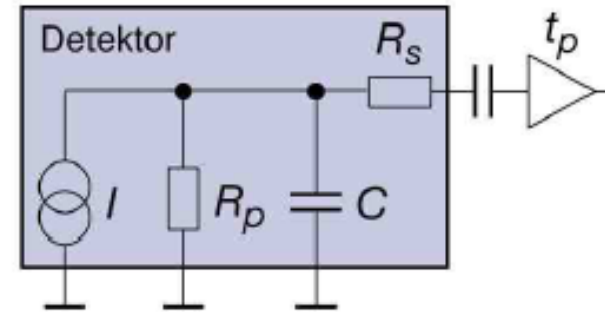
- Typically a 10–1000 e⁻
 - Not always Gaussian

Many inputs:

- Irradiation
- Readout speed
- Leakage Current: $ENC_I \propto \sqrt{I}$
- Thermal Noise: $ENC_I \propto \sqrt{(k_B T/R)}$
- Capacitive noise: $ENC_C \propto C_d$

Noises add quadratically

$$ENC = \sqrt{ENC_C^2 + ENC_I^2 + ENC_{R_p}^2 + ENC_{R_s}^2}$$



Alternate circuit diagram of a silicon detector.

A Brief History of Silicon-Tungsten Calorimeters

Beam & Luminosity Monitors

First applications of Silicon Sampling Calorimeters (to my knowledge): 1984

– Small EM calorimeters around beam tubes to detect Bhabha electrons $e^+e^- \rightarrow e^+e^-$

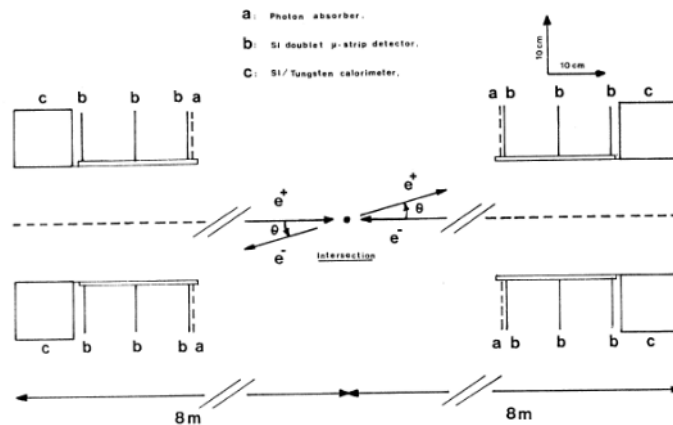
- Very precise positioning
- Fast return for beam positioning & lumi tuning
- Perfect for 1st applications

– SICAPO (Si CALorimeter & POLarimeter) collaboration

- Prototypes as Si/W & Si/U calorimeters (1986)
- In view of LEP

G. Barbiellini¹⁾, G. Cecchet²⁾, J. Y. Hemery³⁾, F. Lemeilleur³⁾, P. G. Rancoita⁴⁾,
A. Seidman⁵⁾ and M. Zilka⁵⁾:

SILICON/TUNGSTEN CALORIMETER AS LUMINOSITY MONITOR



Beam & Lumi: LEP experiments

PICASSO collaboration

- \Rightarrow 4 detector for each LEP experiment
 - Pad = $5 \times 5 \text{ cm}^2$, $300 \mu\text{m}$ at $6X_0 + n \times 2X_0$

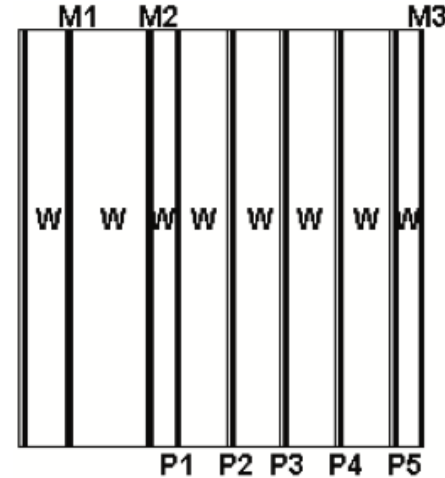
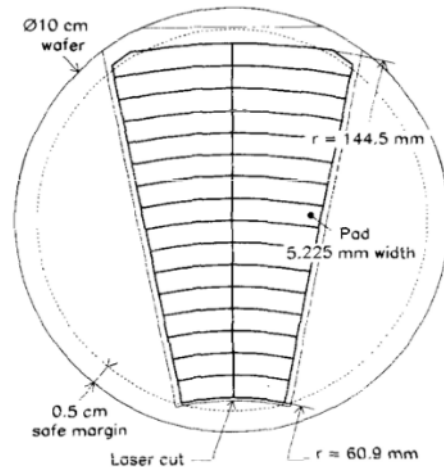
OPAL–Si/W,

DELPHI–VSAT

- Square design

ALEPH–SiCAL,

- $300 \mu\text{m}$ (Canberra)
- Round around beam pipe:



M1,2,3 = Silicon strip detectors
P1–5 = Si Pad det

Beam & Lumi : SLD

SLD detector at SLAC Linear Collider (SLC): 1991

- 300 μm Si + 90% W alloy, $17.5X_0$
- LMSAT : 23 layers, 300 μm & MASC: 10 layers, 300 μm
 - Sampling $0.86 X_0$ & $1.74 X_0$
 - 2 longitudinal sections $\times \sim 1 \times 1 \text{ cm}^2$ cells
- Readout by ribbon cables
- $\sigma(E)/E = 20\%/\sqrt{E}$

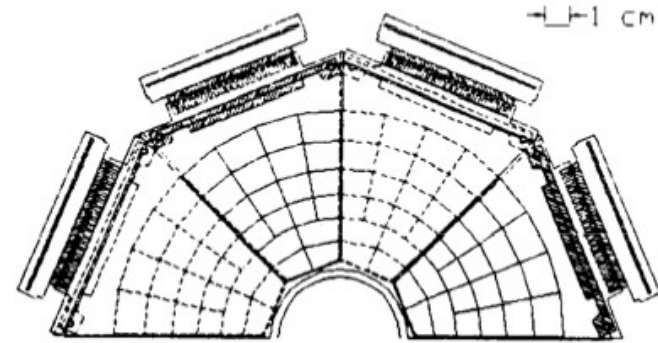
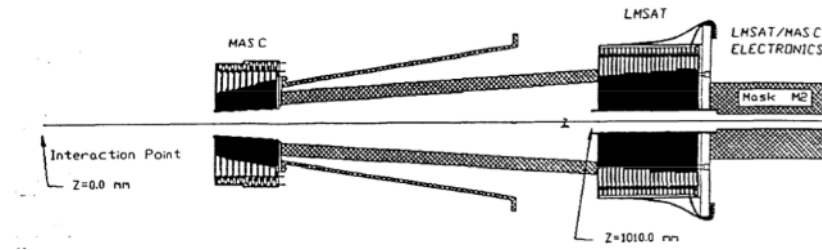


Figure 2. Front face of one LMSAT module as seen from the IP. Detectors shown with dashed lines have their ground planes facing away from the IP.



FCAL Collaboration: LumiCal & BeamCAL

LumiCal :

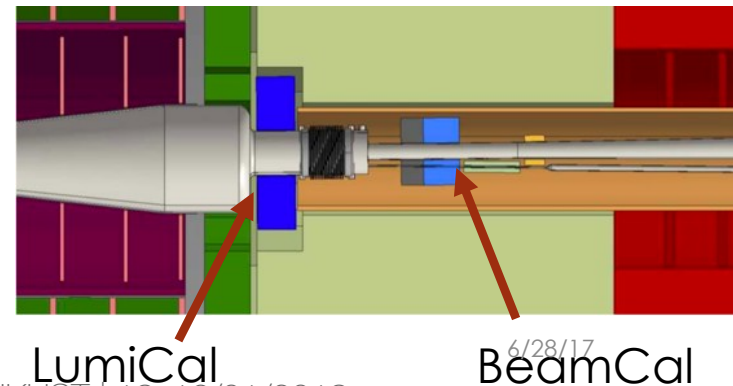
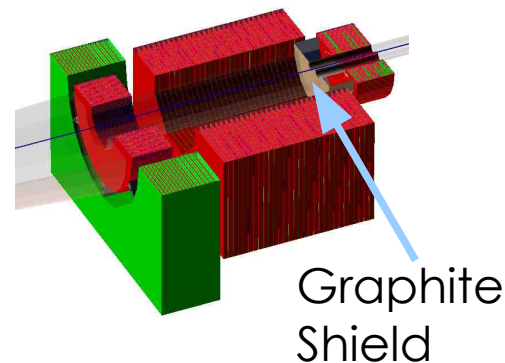
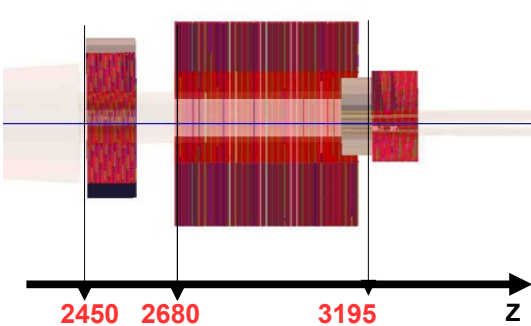
- Precise integrated luminosity measurements (Bhabha events)
- Extend calorimetric coverage to small polar angles. Important for physics analysis

LHCal :

- Extend the hadronic calorimeter coverage
- 29 layers of 16mm thickness. Absorber : tungsten or iron

BeamCal :

- Measure instant Luminosity. Feedback for beamtuning
 - providing supplementary beam diagnostics information extracted from the pattern of incoherent-pair energy depositions
- tagging of high energy electrons to suppress backgrounds to potential BSM process
 - shielding of the accelerator components from the beam-induced background
- Sampling calorimeter based on tungsten plates
 - 30 layers for ILC, 40 layers for CLIC
- Due to large dose, rad hard sensors (GaAs, Diamond, Sapphire)

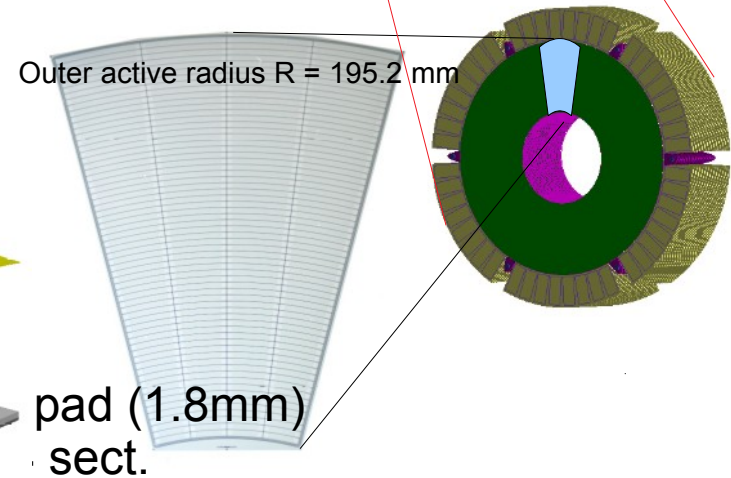
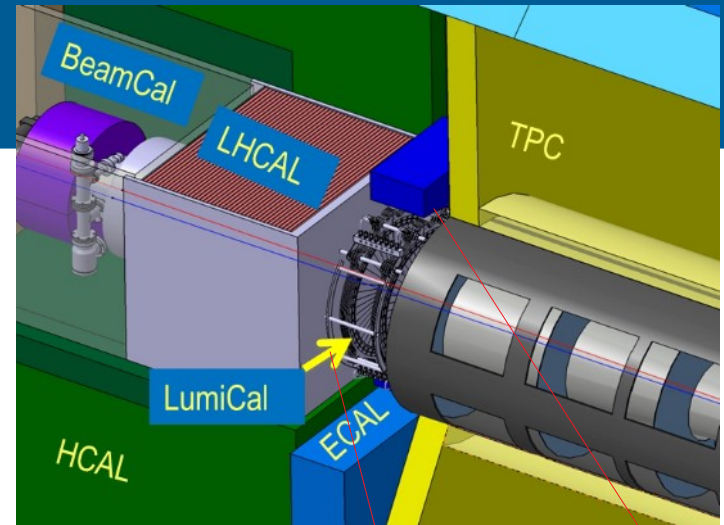
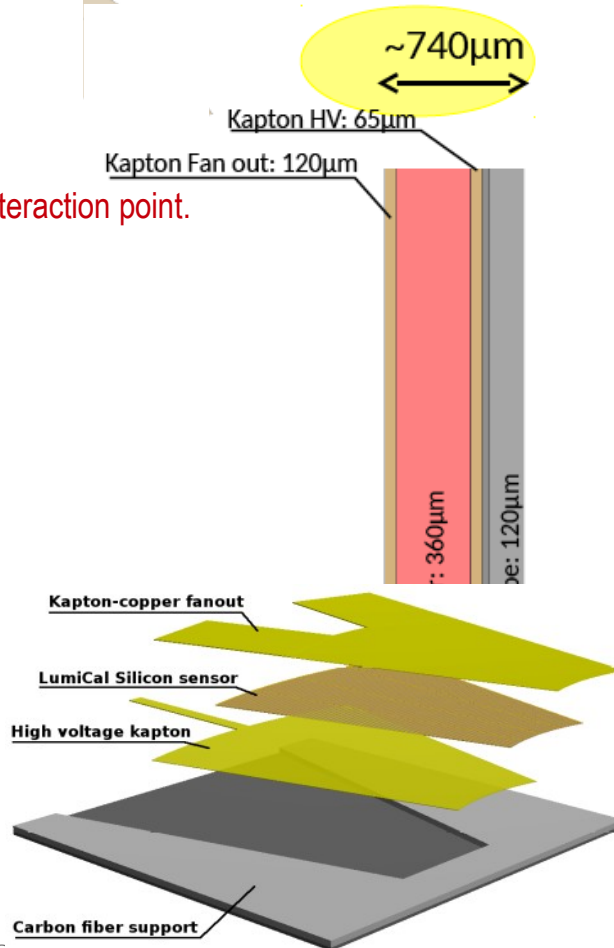


FCAL collaboration: LumiCal

Bhabha measurement

Sampling ECAL

- symmetrically on both sides at $\sim 2.5\text{m}$ from the interaction point.
- 30 layers of 3.5 mm thick tungsten plates ($1X_0$)
- 320 μm Si (p+ implants in n-type bulk)
 - DC coupling to readout
 - through Kapton foils glued on wafer
- Ultra-thin design: $R_M=12\text{mm}$ expected
 - Bhabha + $\gamma\gamma$ background



Digital-ECAL: ALICE-FOCAL

Mat from T. Peitzmann, Gert-Jan Nooren

Use photons to measure PDF saturation in proton/nucleus

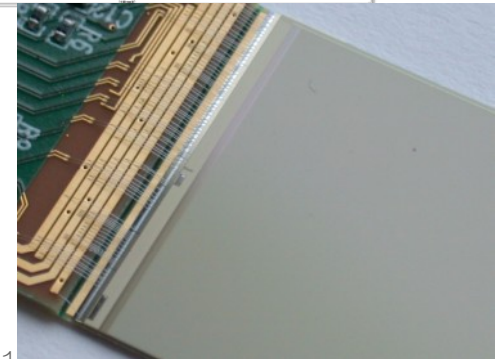
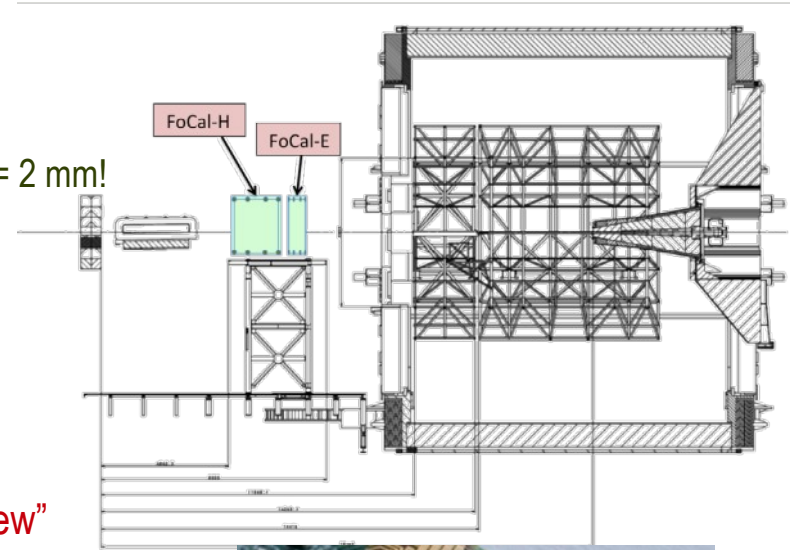
- High π_0 's background
 - two-photon separation from π_0 decay ($p_T = 10$ GeV/c, $y = 4.5$, $\alpha = 0.5$) is $d = 2$ mm!

Ultra-granular ECAL for γ and π_0 measurement

- Ideally: at $z \approx 7$ m (outside solenoid magnet), $3.3 < \eta < 5.3$
 - space to add hadronic calorimeter
 - under internal discussion, possible installation in LS3
- advantage in ALICE: forward region not instrumented, “unobstructed view”

MAPs (Monolithic Active Pixel Sensor) in calorimeter \Rightarrow test of concept in prototype

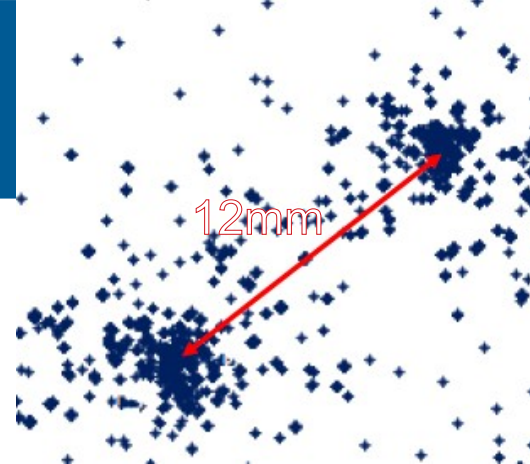
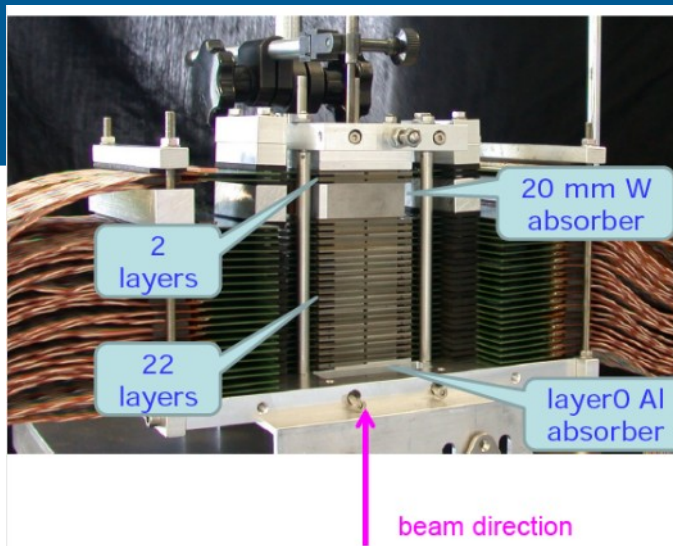
- PHASE2/MIMOSA23 sensor (VTX)
 - 30 μ m pixels: max. occupancy 1111/mm²
 - 642 μ s integration time \Rightarrow beam tests
 - 15 μ m active layer \Rightarrow very small sampling fraction



DECAL: FOCAL

Prototype:

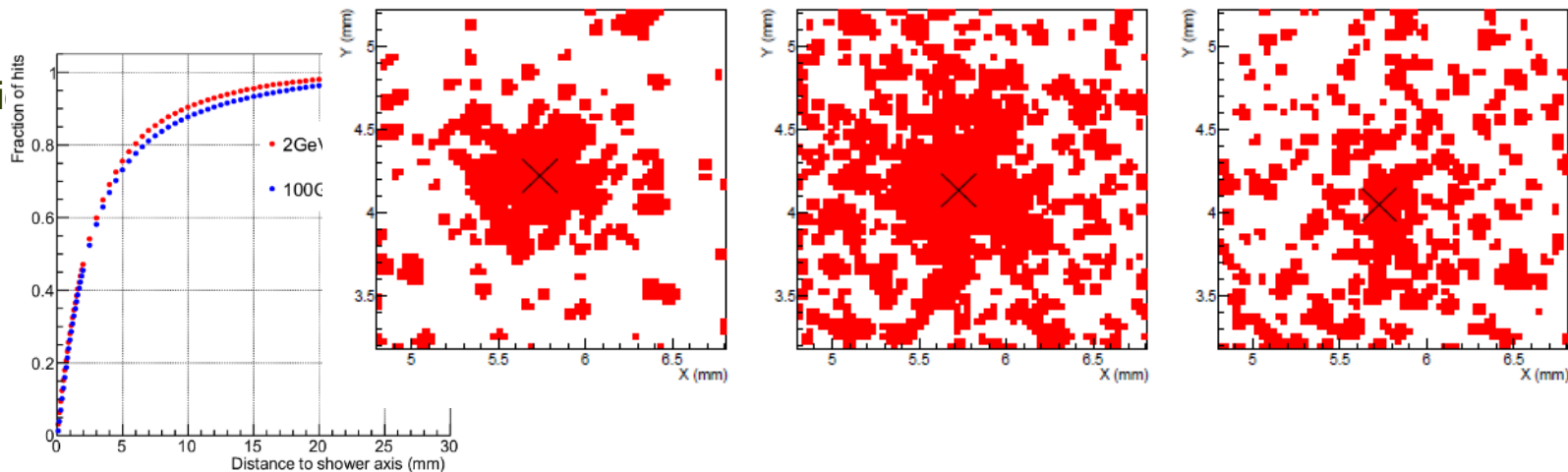
- 24 layers \times 4 sensors
 - 39 M pixels =, full readout
- 28 X_0



Digital calorimetry

- $E \propto$ cluster size
 - Mips $\uparrow \Rightarrow$ Symmetri
- Saturation effects

$$R_M = 11\text{mm}$$



Balloons & Space experiments

Possibility to work in vacuum + Large range of working temperature

Experiments γ -astronomy by photon to pair conversion in W + tracking in Si (Strips)

- **AGILE: launch 2007**
 - 13 trays. 10 first trays with W layers and 24 silicon microstrip plane layer - of 9.5×9.5 cm² area, 121 μ m pitch and 421 μ m
 - X-ray imager (for imaging in the range 18-60 keV) is a coded-mask system made of a silicon detector plane and a thin tungsten mask.
- **Fermi-LAT (next slide)**

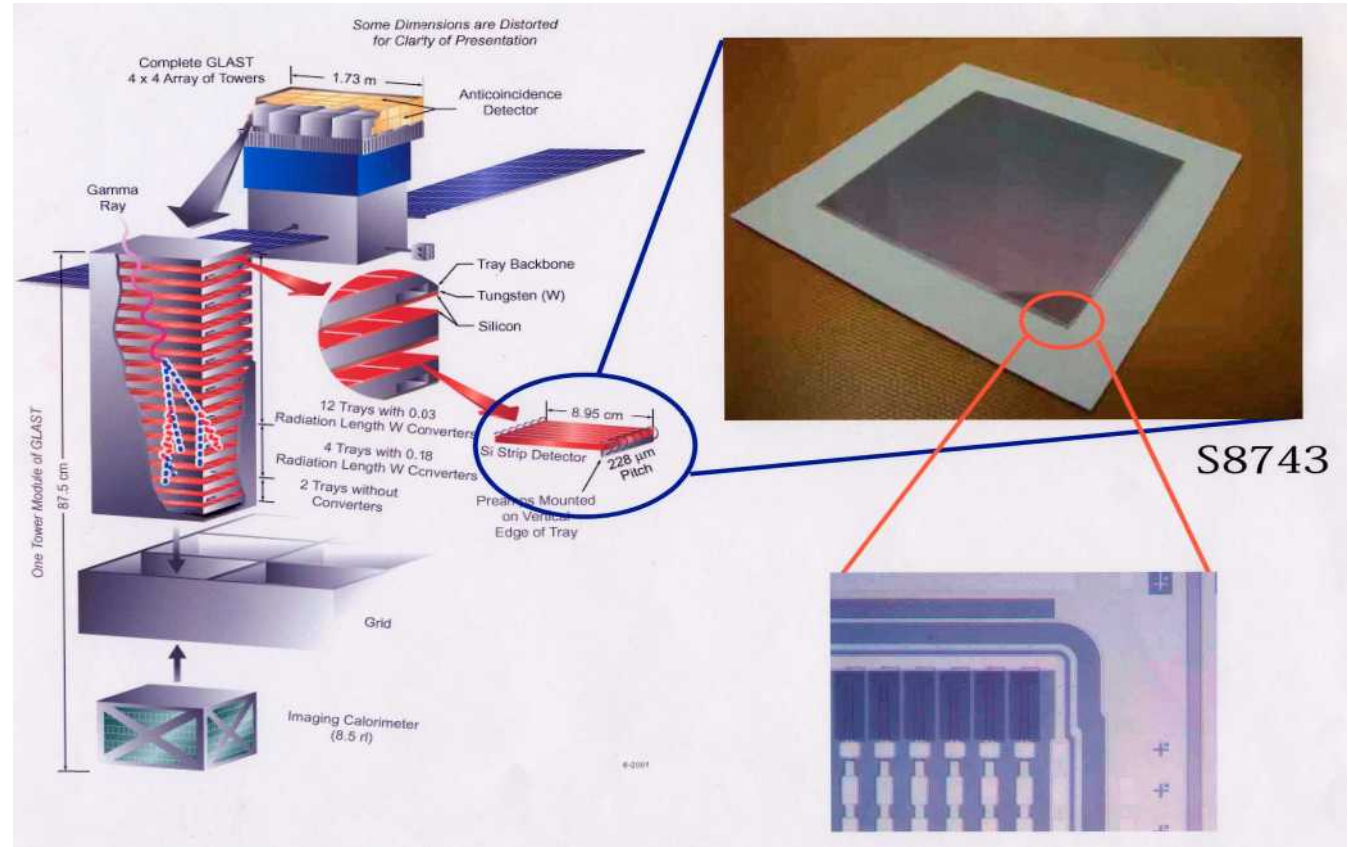
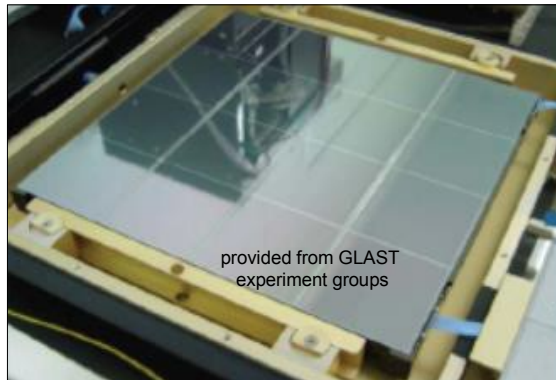
Cosmic Ray studies

- **NINA (New Instrument for Nuclear Analysis): 1997**
 - Space telescope: 16 pairs of silicon sensitive planes x - y readout planes.
 - Sensors: 60×60 mm² \times 380 μ m and is divided into 16 strips of 3.6 mm pitch.
 - Stopping by N₂ gas at 1 atm.
- **Nucleon: ???**
- **PAMELA (2007):**

Fermi (GLAST) - almost a calorimeter

Pair conversion telescope

- Launch in 2008
- Si/W : $410 \mu\text{m} \times 89.5 \times 89.5 \text{ mm}^2$
- 16 towers $\Rightarrow 37 \times 37 \text{ cm}^2$ active area
- 70 m^2 of strip silicon ($228 \mu\text{m}$ pitch), 9216 Wafers, 880k channels



PAMELA

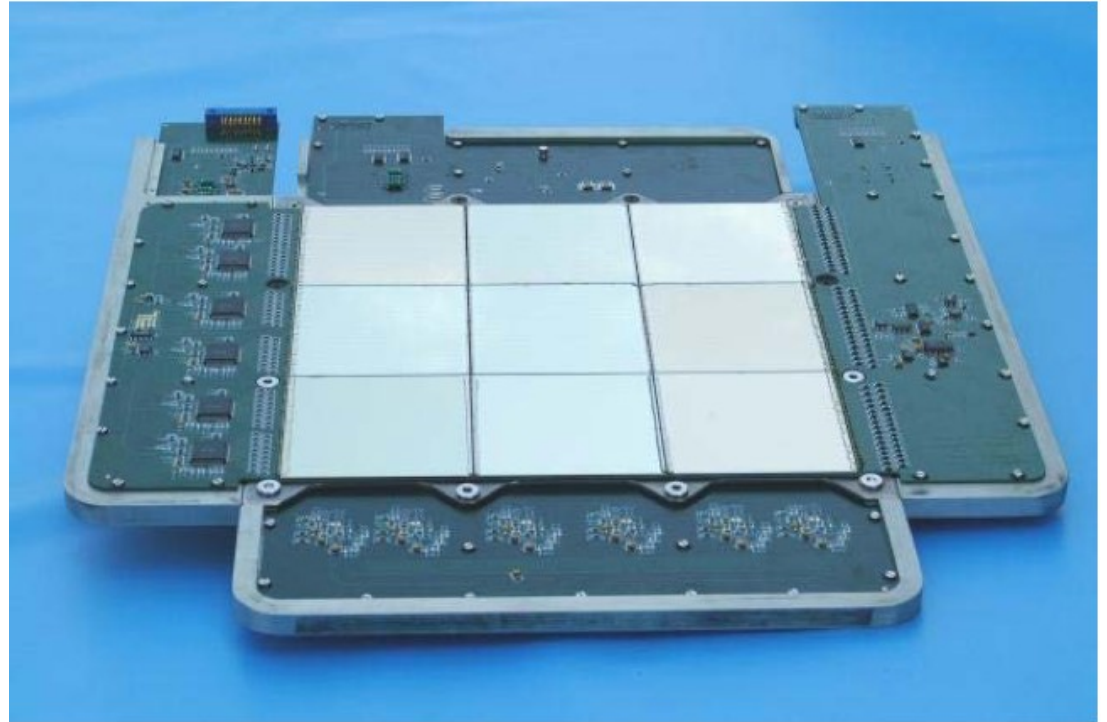
Satellite

- Launched June 2006
- Cosmics rays (e^\pm , p , $pbar$, light N), DM
 - 50 meV – 100's GeV

SiW-calorimeter

- 22 Tungsten plates of 0.26 cm (16.3 X_0 and 0.6 λ)
 - 0,74 X_0 per plane
- 44 silicon layers, of 3×3 Wafers
 - 80 × 80 × 0.380 mm³
 - Interleaved x and y strips
 - and segmented into 32 strips, (96 strips/plane)

+ tails catcher (Scint.) + Si Tracker (perm. 0.43T)



Si(W)-ECAL in Large Experiments

no realisation (yet!) but many projects nearing realisation

SSC proposal (~1984), LHC : no

e⁺e⁻ colliders: ILC ⇒ CLIC, CEPC, FCC-ee: 2028-30 ?

– **SiD full SiW-ECAL**

- Design & prototype

– **ILD: full SiW-ECAL**

- ILC , CEPC, CLIC options
- CALICE collaboration
 - Physical prototype
 - Technological prototype

Challenges :
- large numbers of ch.
- integration
- cooling
- uniformity

Heavy Ion experiments (only as projects)

– PHENIX, AFTER, ALICE

- Tagging of many photons

**See presentation
from Huaqiao Zhang**

CMS-HGCAL prototype: 2026 ?

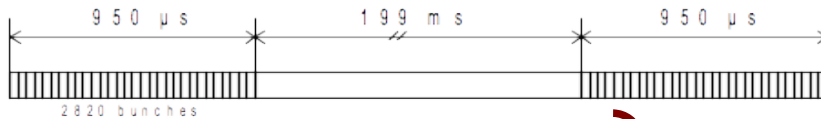
- Large area PAD detectors
- Very Precise Timing → 5D calorimetry

(ATLAS-HGTD)

Current Studies

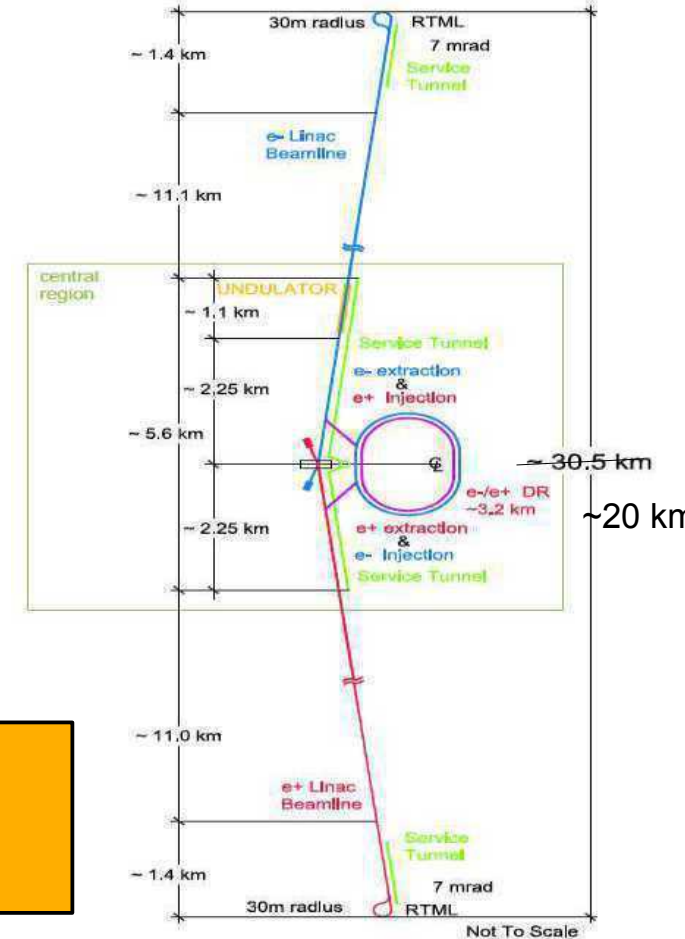
ILC parameters

Max. Center-of-mass energy	250–500 (90)	GeV
Peak Luminosity	$0,8–3 \times 10^{34}$	$1/\text{cm}^2\text{s}$
Beam Current	5.8	mA
Repetition rate	5 (10)	Hz
Average accelerating gradient	31.5	MV/m
Beam pulse length	0.95	ms
Total Site Length	31	km
Total AC Power Consumption	120-300	MW



- Time between collisions : 350–700 ns
- Trains of 1300–2700 Bunches
- Low detector occupancy
- Low bgd : $e^+e^- \rightarrow qq \sim 0.1 / \text{BC}$
 $\rightarrow \gamma\gamma \rightarrow X \sim 200 / \text{BX}$

- High B field
- Trigger-less
- Power Pulsing ($\leq 1\%$)
- Differed readout



Constraints on detectors:

Basis: sep of $H \rightarrow WW/ZZ \rightarrow 4j$

– $\sigma_z/M_z \sim \sigma_W/M_W \sim 2.7\% \oplus 2.75\sigma_{sep}$

$\Rightarrow \sigma_E/E \text{ (jets)} < 3.8\%$

– $Sign \sim S/\sqrt{B} \sim (resol)^{-1/2}$
 $60\%/\sqrt{E} \rightarrow 30\%/\sqrt{E} \Leftrightarrow +\sim 40\% L$

Large TPC

- Precision and low X_0 budget
- Pattern recognition

High precision on Si trackers

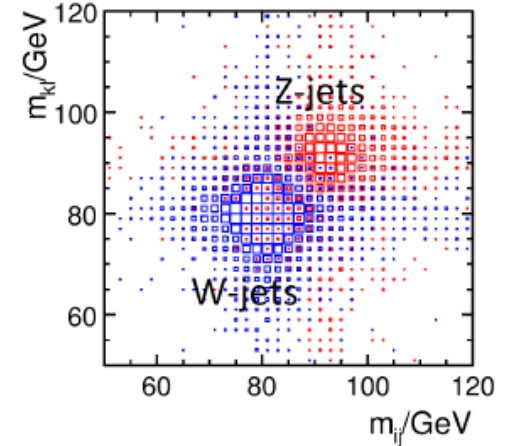
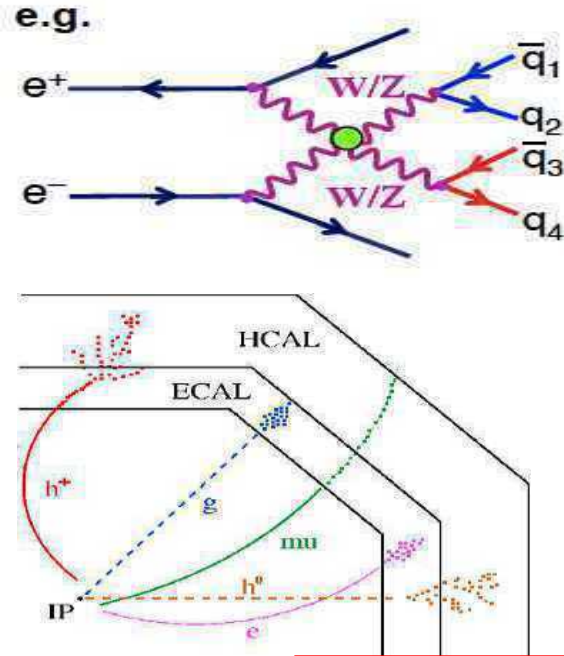
- Tagging of beauty and charm

Large acceptance

Fwd Calorimetry:

- lumi, veto, beam monitoring

Imaging Calorimetry



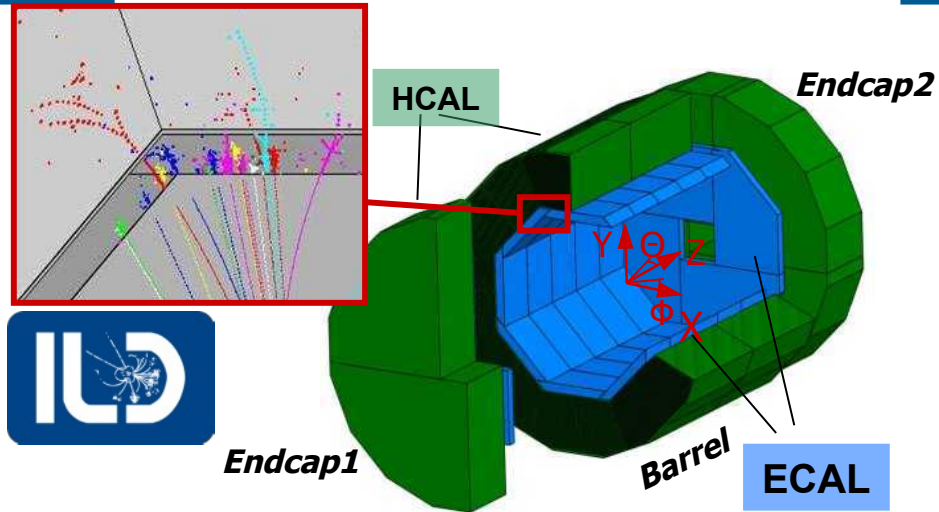
Photons in jets
Tau physics (γ vs π_0)
 1/3 of IA in ECAL

Particle Flow Algorithms :

- Jets = 65% charged Tracks + 25% γ ECAL + 10% h^0 CALO's
- TPC $\delta p/p \sim 5 \cdot 10^{-5}$; VTX $\sigma_{x,y,z} \sim 10 \mu m$

H. Videau and J. C. Brient, "Calorimetry optimised for jets," in Proc. 10th International Conference on Calorimetry in High Energy Physics (CALOR 2002), Pasadena, California. March, 2002.

An Ultra-Granular SiW-ECAL for experiments



Particle Flow optimised calorimetry

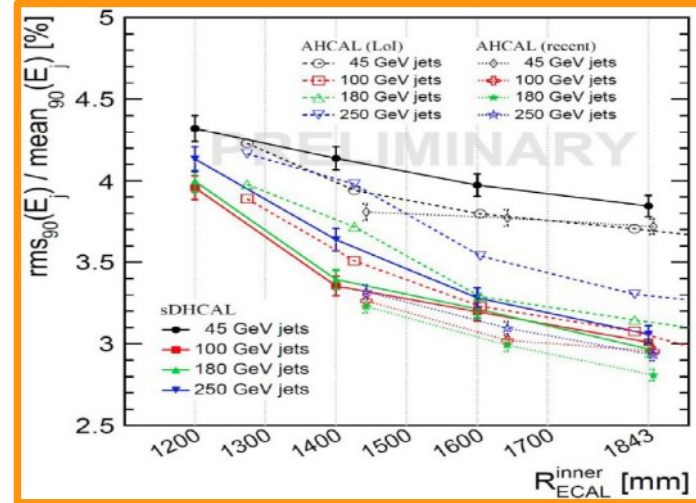
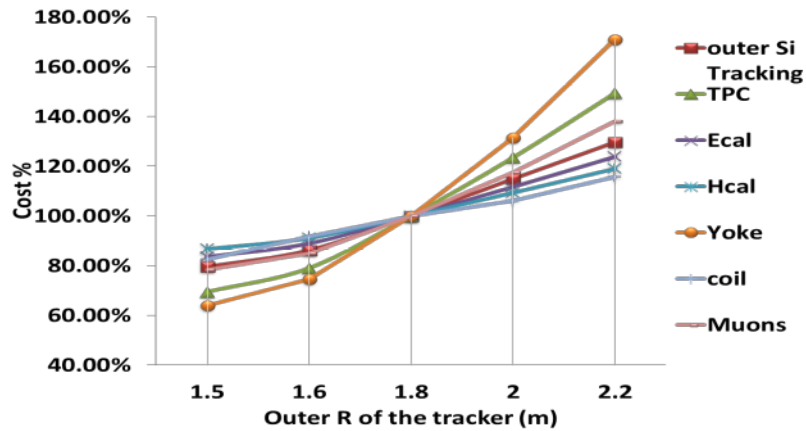
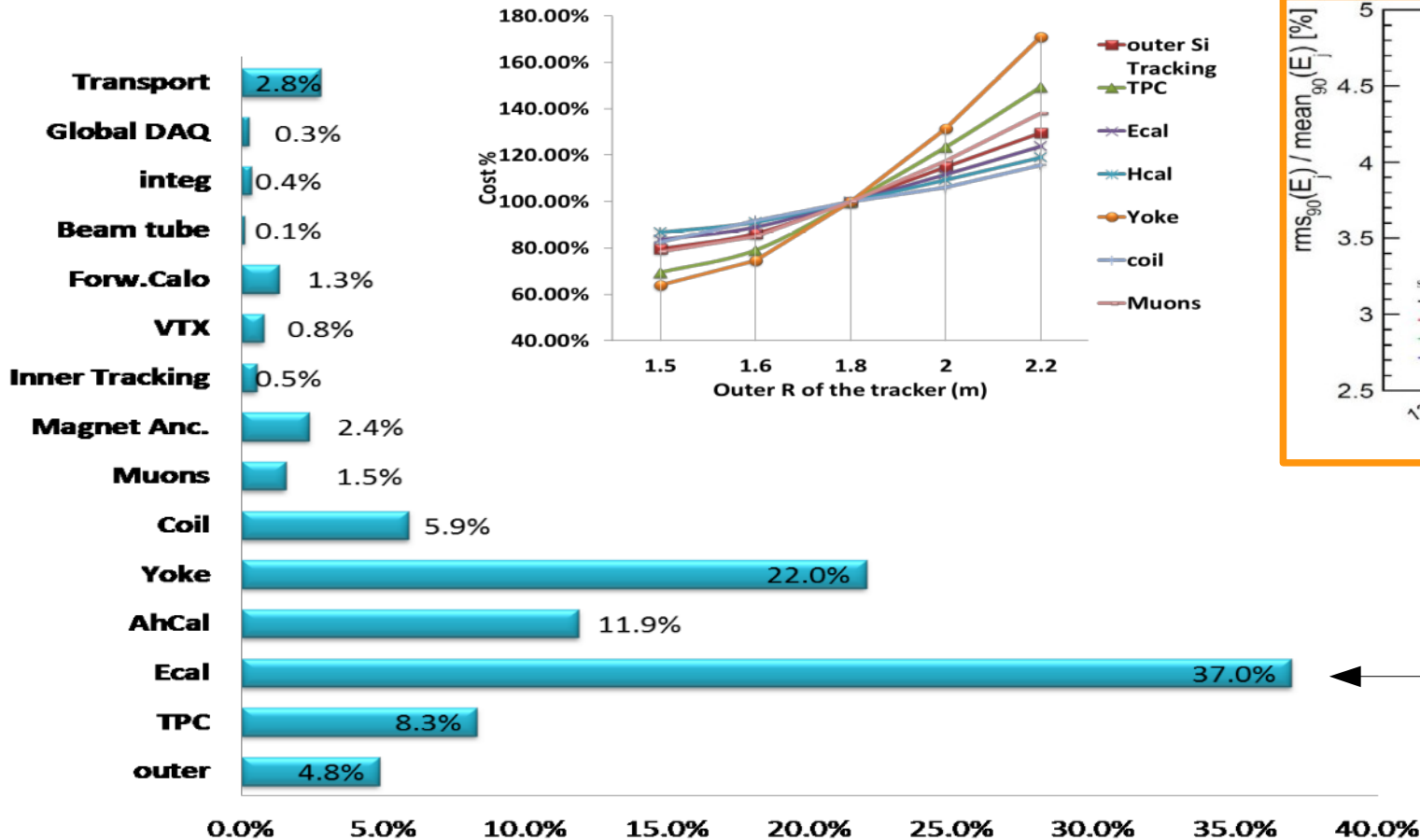
- **Standard requirements**
 - Hermeticity, Resolution, Uniformity & Stability (E , (θ, φ) , t)
- **PFlow requirements:**
 - Extremely high granularity
 - Compacity (density)

SiW+CFRC baseline choice for future Lepton Colliders:

- **Tungsten as absorber material**
 - $X_0 = 3.5$ mm, $R_M = 9$ mm, $\lambda_I = 96$ mm
 - Narrow showers**
 - Assures compact design**
- **Silicon as active material**
 - Support compact design**
 - Allows for ~any pixelisation**
 - Robust technology**
 - Excellent signal/noise ratio: ≥ 10**
 - Intrinsic stability (vs environment, aging)**
 - Albeit expensive...**
- **Tungsten–Carbon alveolar structure**
 - Minimal structural dead-spaces**
 - Scalability**

Cost Structure of ILD

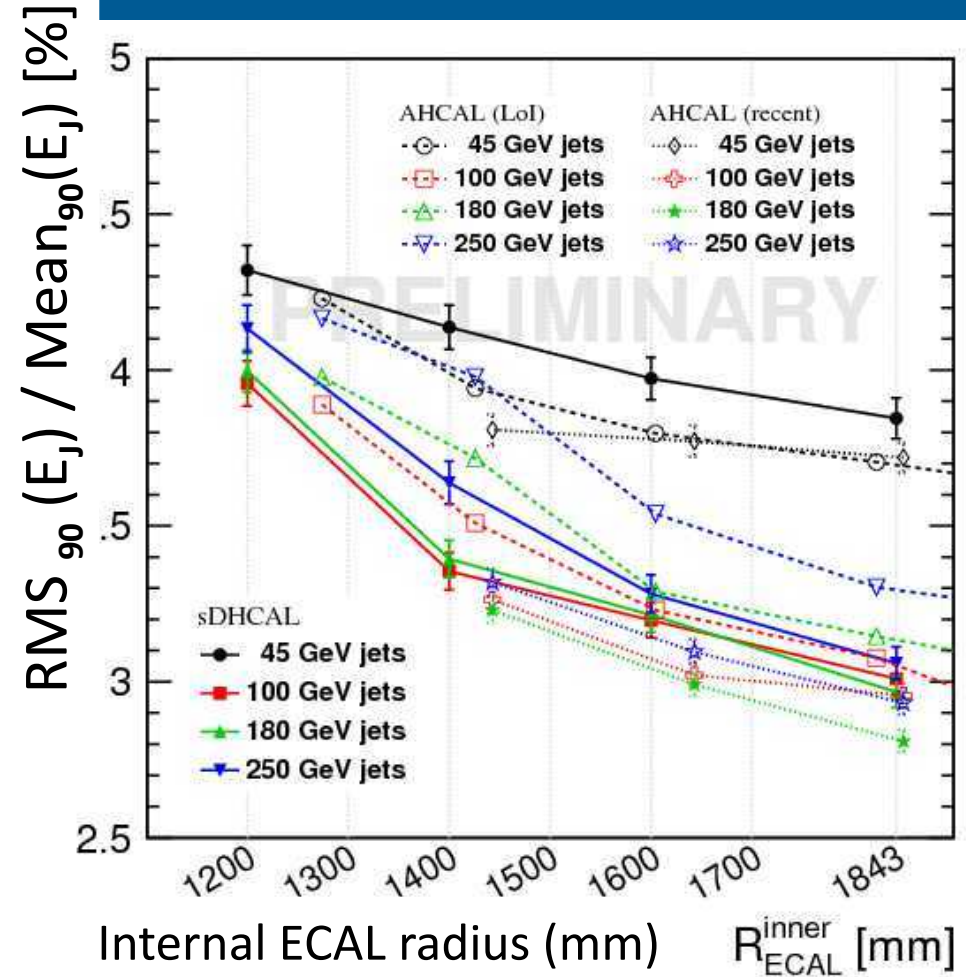
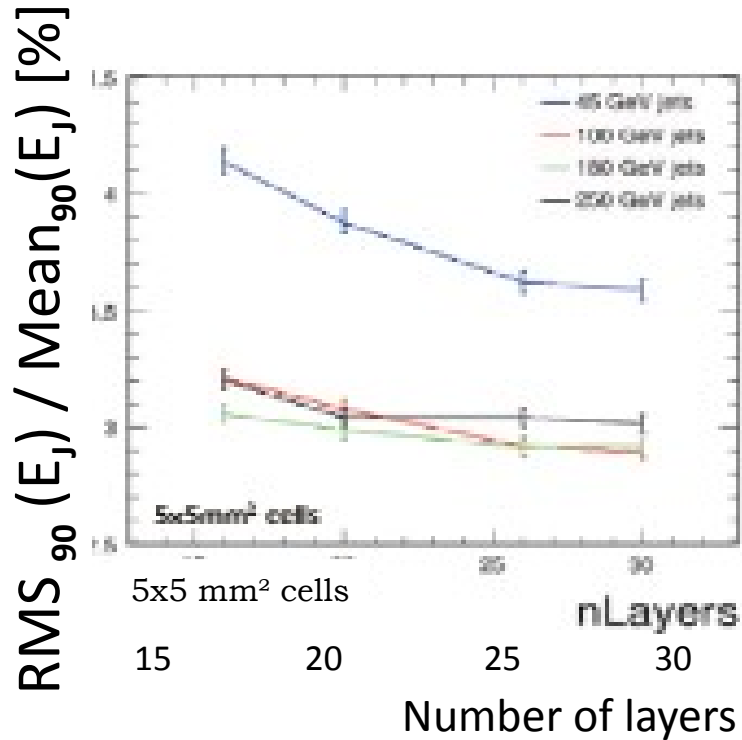
For ILD see presentation from Imad



← Full Silicon option

Parameter optimisations

A new feeling , **AFTER** the staging proposal



Reduced number of Layers

Going from 30 to 22 layers

- Reduction of cost; (small) reduction of R_M ; increase of Energy resolution
 - “better separation at the expense of the intrinsic resolution”

Increasing the Si thickness to 725 μm , if really feasible (next slide)

Energy resolution $\sigma(E)/E$:

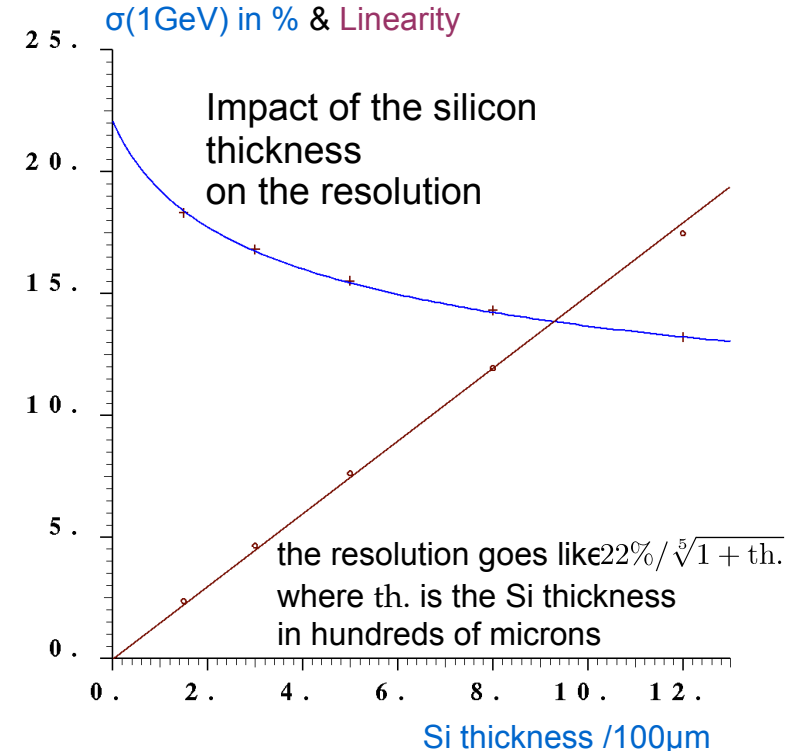
- for 22 layers w.r.t. 30: +16.8%
- with 725 μm w.r.t. 500 μm : -6.1%

ECal thickness = 190.1 mm (close to 185 mm of DBD).

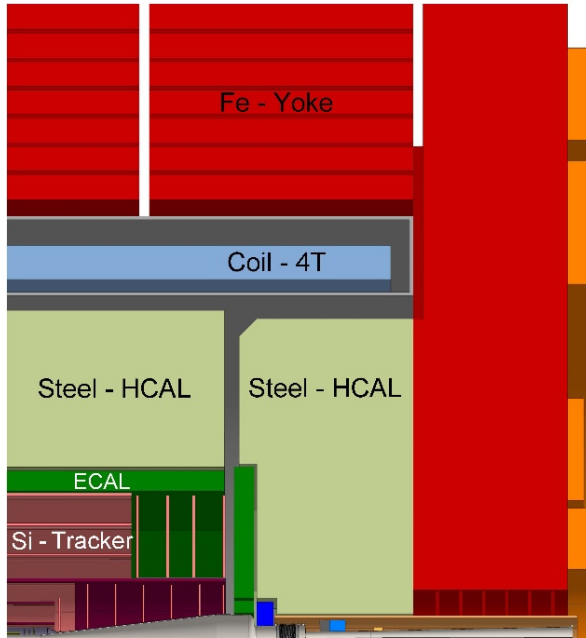
- 22 layers = 14 layers with 2.8mm thickness
+ 8 layers with 5.6mm shared between structure and slabs.

Study needed on separation, resolution and efficiency performances at low energy.

- JER : $\sigma(E_j)/E_j + 10\%$ for 20 layers (500 μm).



CLIC calorimeters



ECAL Optimization:

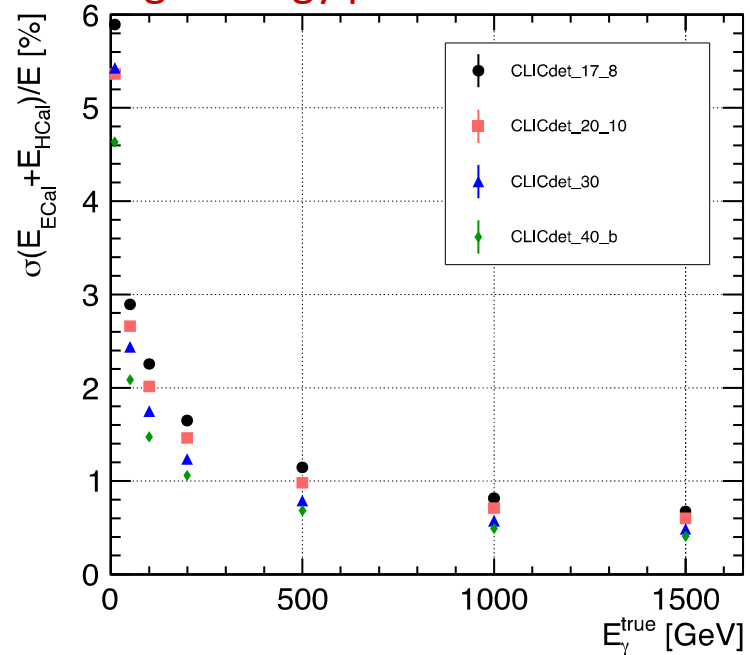
40 layers uniform fine sampling silicon-tungsten plates

(1.9 mm W, 5x5 mm² silicon cells)

22 X₀ (1 λ_i) total thickness

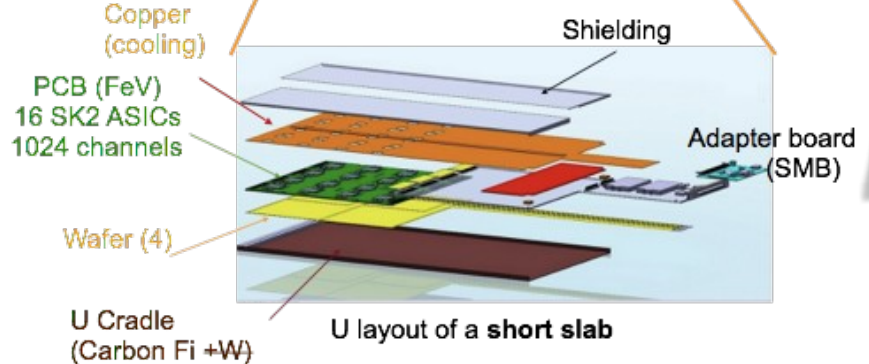
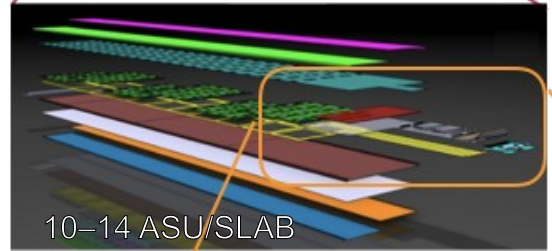
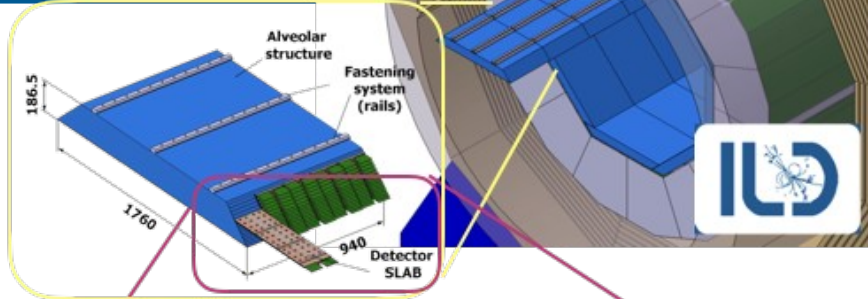
18/01/2018

Energy Resolution for central high energy photons



Large Scale Building

ILD & SiW-ECAL barrel



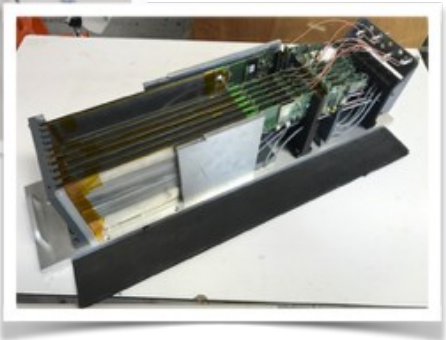
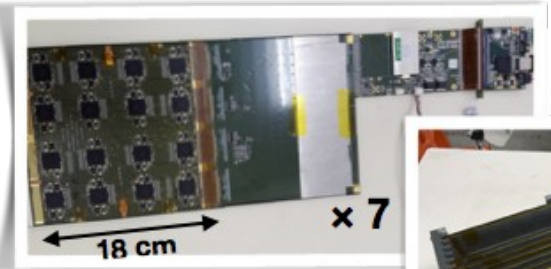
ILD SiW-ECAL

Prototyped*

~10,000 SLAB's	~0.1
100,000 ASU's	~20
400,000 Wafers	~350
1,600,000 ASIC's	~1000
100,000,000 channels	~20000

* incl. Physical Prototype

+ Mechanics , Cooling, Integration, ...



CALICE SiW ECAL: Physics & Technological prototypes



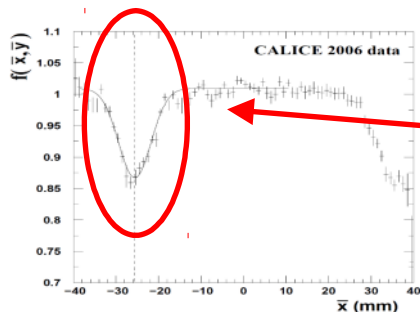
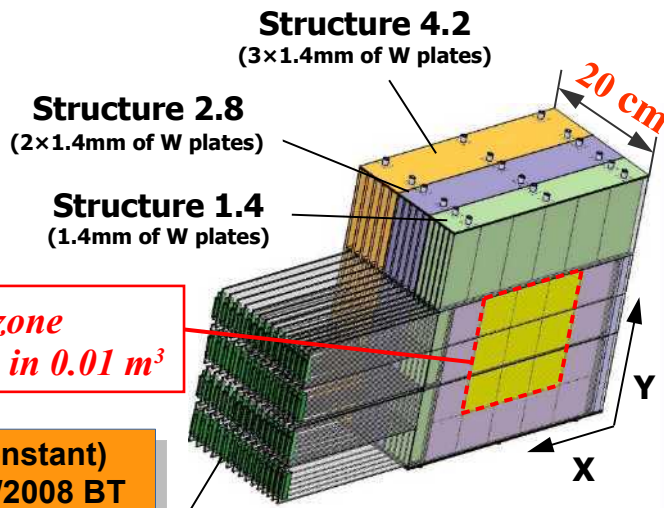
Physics prototype: 2005–2011

PFA proof of concept with comparison to MC (PandoraPFA etc.)
Electronics outside

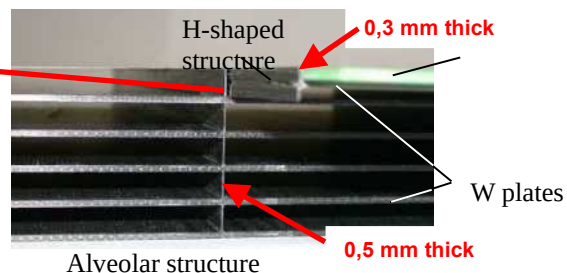
- $1 \times 1 \text{ cm}^2$ pixels

Active zone
~10000 pixels in 0.01 m^3

16.5% (stochastic) 1–2% (constant) with 1–45 GeV e^-/e^+ at 2006/2008 BT



Detector slab (x30)



Technological prototype



Embedded electronics

- SKIROC2 analog/digital ASICs
 - auto-triggered, zero suppr., PP
- pixels $5 \times 5 \text{ mm}^2$

Performances: photon reconstruction confusion studies

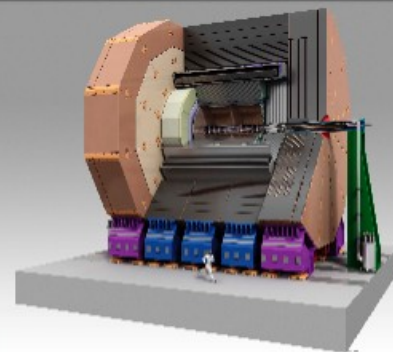
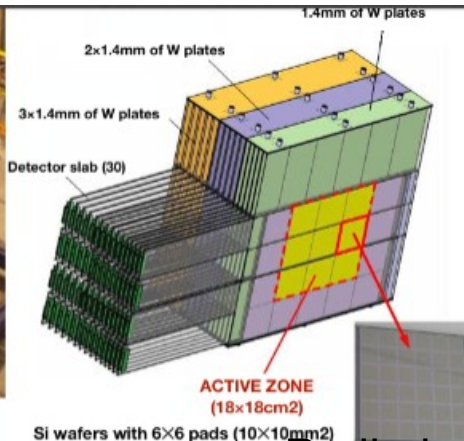
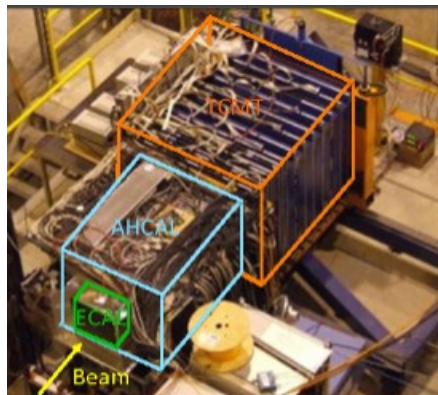


“raw performances”

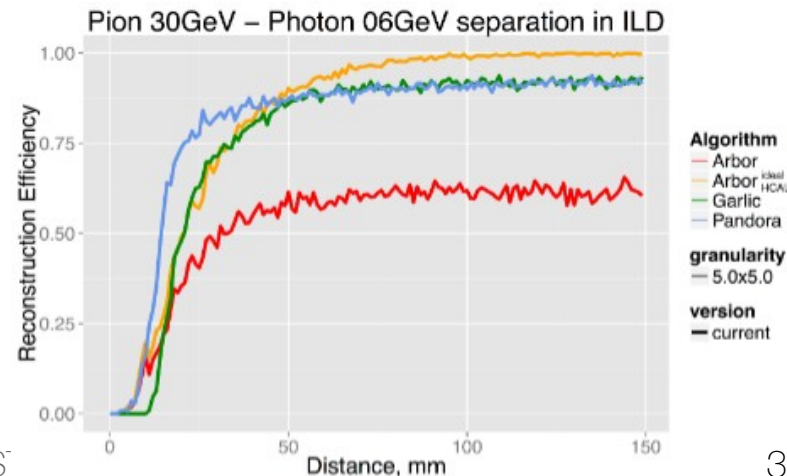
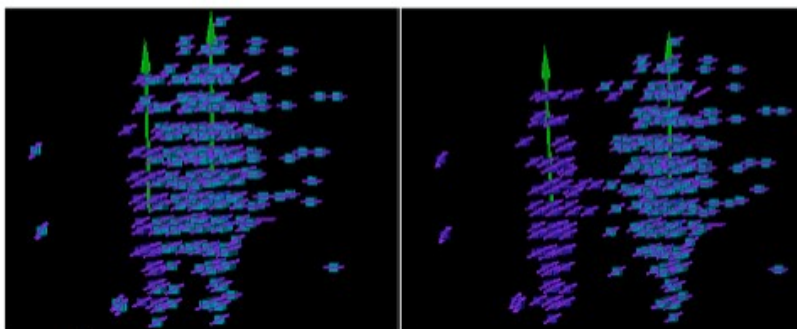
- Efficiency vs separation distance
- EM vs EM (e / γ)
- EM vs π

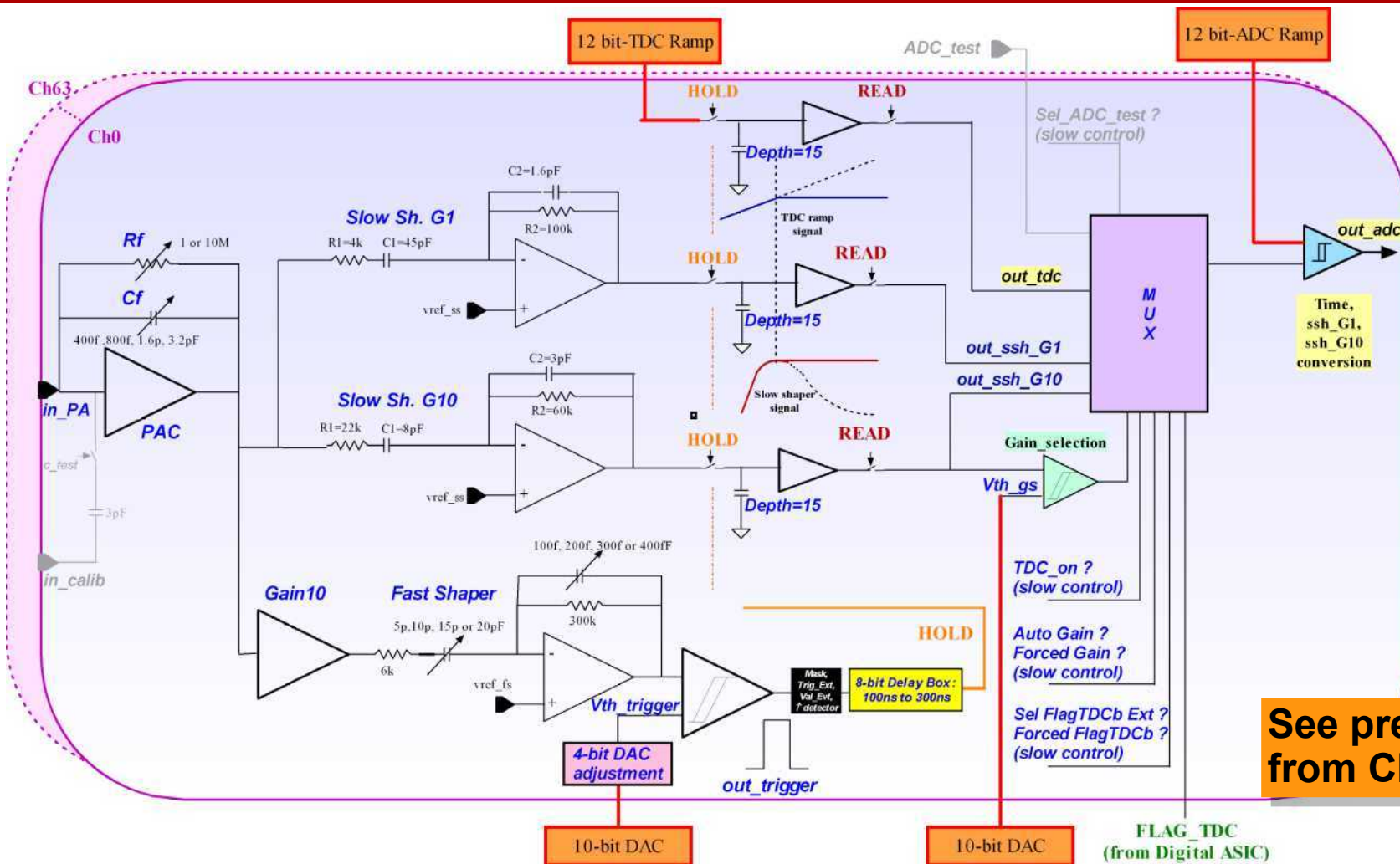
Using Particle Flow Algorithms

- PandoraPFA, Arbor, GARLIC



Preliminary





Similar to
SiD Kpix

- 64 channels
- Preamp
+ 2 (auto)Gains
+ TDC
- Auto-triggered
- 15 memory
- Low consumption
- Power-pulsed

- Not final chip
(full 0-suppr.)
- Not suitable
for CEPC

**See presentation
from Ch. De la Taille**

ILD Building blocks: SLAB's & ASU's

R&D for “mass production” and QA

- Quality tests & preparation of large production
- Modularity → ASU & SLABs
- Choice of square wafers
 - (≠ from hex: SiD, CMS HGCal)

Numbers ($R_{\text{ECAL}} = 1,8 \text{ m}$, $|Z_{\text{Endcaps}}| = 2,35 \text{ m}$)
(likely to be reduced by 30–40%)

- Barrel modules: 40 (as of today all identical)
- Endcap Modules: 24 (3 types)
- ASUs = ~75,000
 - Wafers ~ 300,000 (2500 m²)
 - VFE chips ~ 1,200,000
 - Channels: 77Mch
- Slabs = 6000 (B) + 3600 (EC) = 9600
 - ≠ lengths and endings

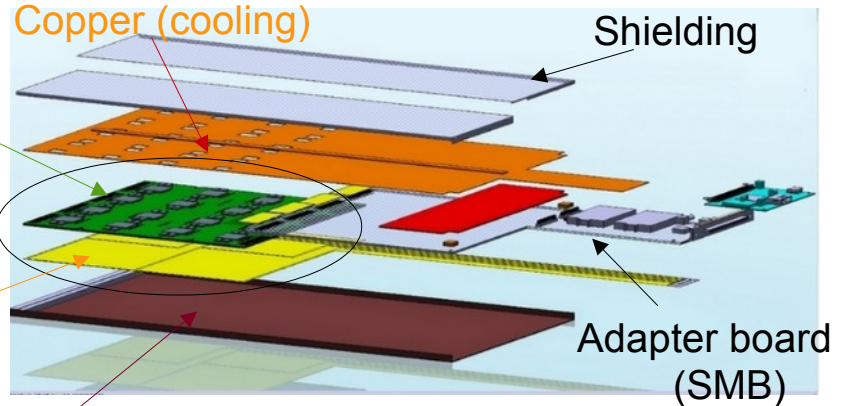
Tests of producibility

Tests of feasibility

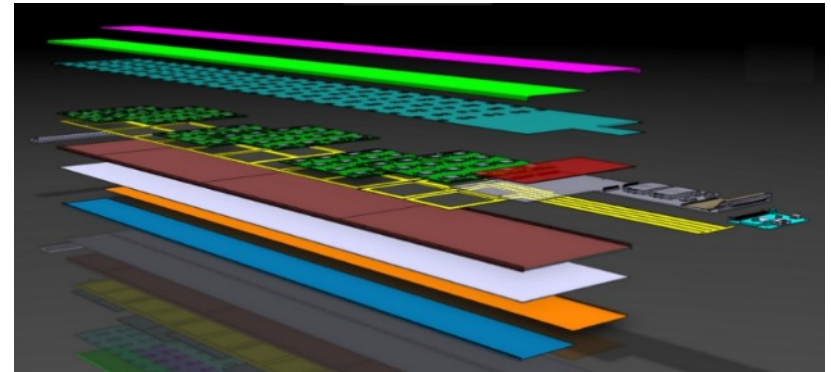
PCB (FeV)
16 SK2 ASICs
1024 channels

ASU

Wafer (4)



Carbon+W U layout of a **short slab**



U layout of a **long slab**

Beam Test of Technological prototype: 7 SLAB's



MIP scan

- Positrons of 3 GeV
- Grid of 9x9 points separated by 2 cm

Data used for pedestal subtraction and energy calibration:

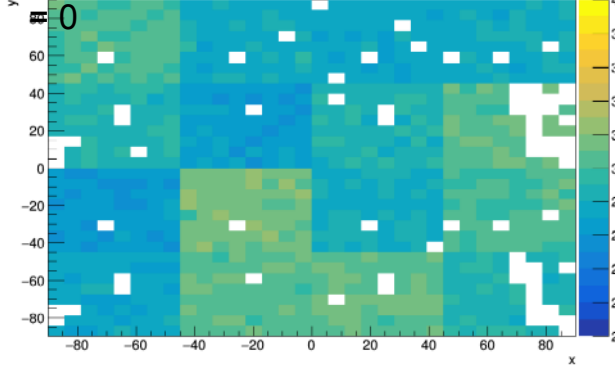
- Pedestal correction done chip/channel/sca wis
- Energy calibration done chip/channel wise

Fit the 98% of available channels.
Channel dispersion of 5%.

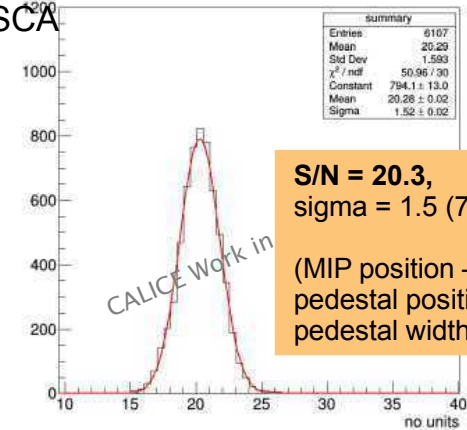
Also 45 degrees inclination run:
MIP value scaling as expected
→ good thresholds choices.

Trigger @ ~1/3 of MIP

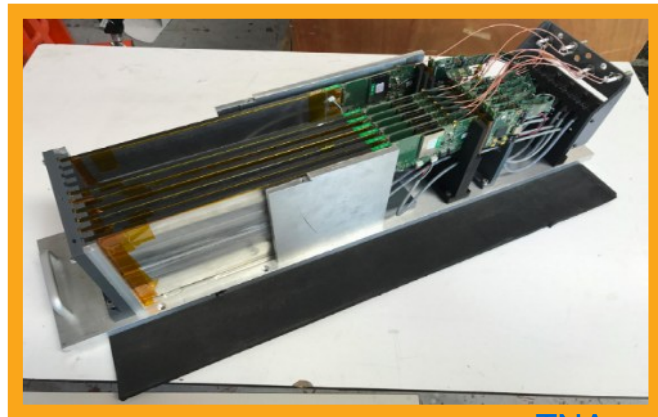
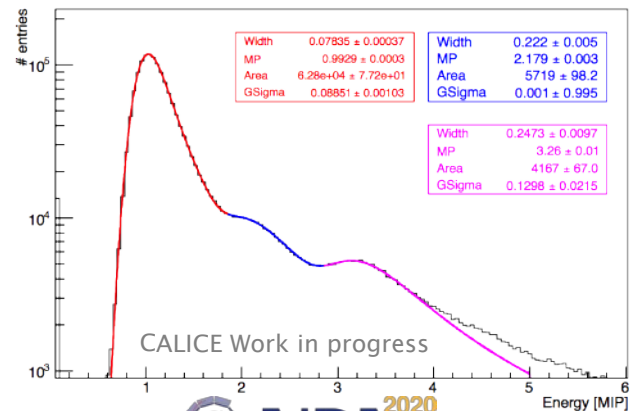
Layer #6, pedestal position (ADC) map for SCA



S_N summary (all slabs)



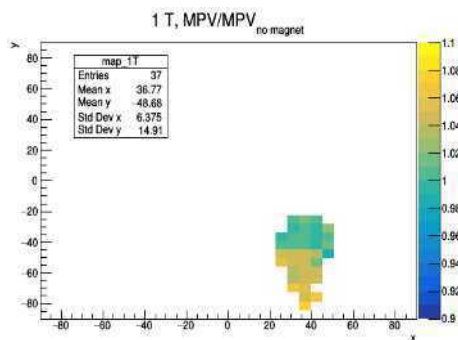
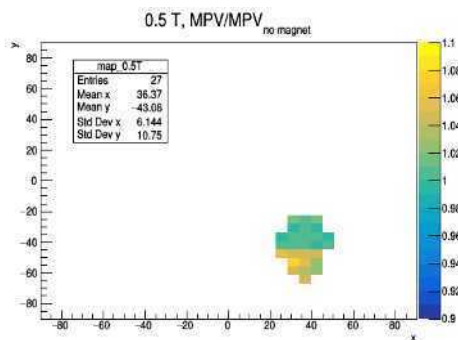
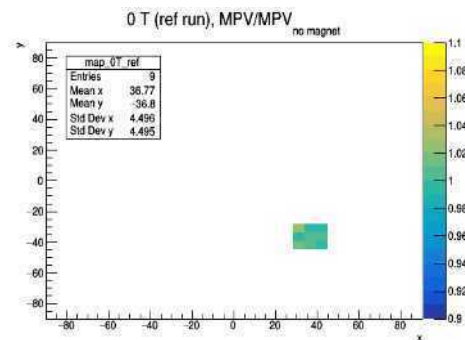
Single cell energy distribution for 3 GeV e⁺ beam w/o absorber



Test in B field

Magnetic field tests

- Single Slab (21, first layer in the full stack)
- (Magnetic field from 0, 0.5, 1 T) \otimes (With and without beam)
 - Same configuration than in the other beam area.
- Not evident failure/loss of performance during visual inspection on the web cam & online monitor.
- ~20 hours of data in total



Test of 'Long Slabs'

Scale to support electronics

- 2+6+4 ASUs = ~3.2 m
- Total access to upper and lower parts
 - Baby wafers (4x4 pixels) on the bottom

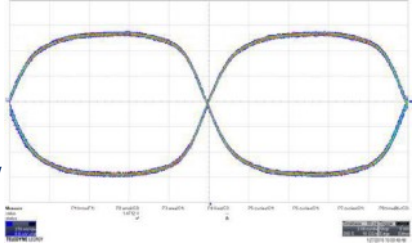
Mechanical characteristics

- Movable: table and to beam test
- Rotatably along long axis (for beam test)
- Rigidity : $\leq \sim 1$ mm per ASU
- No electrical contacts scale / cards

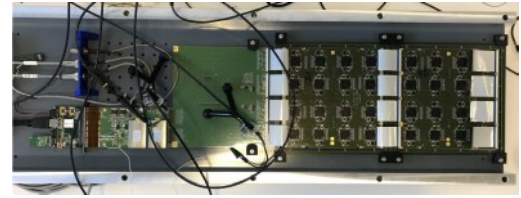
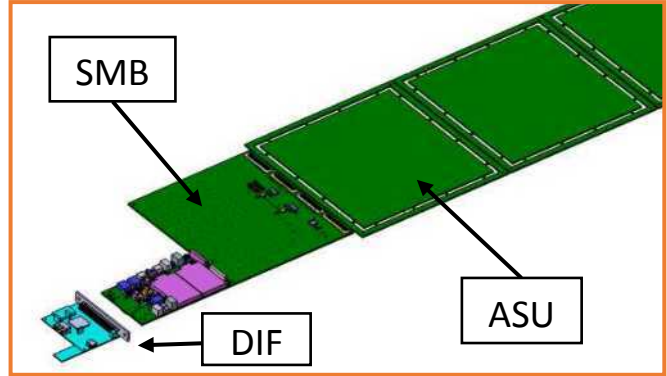
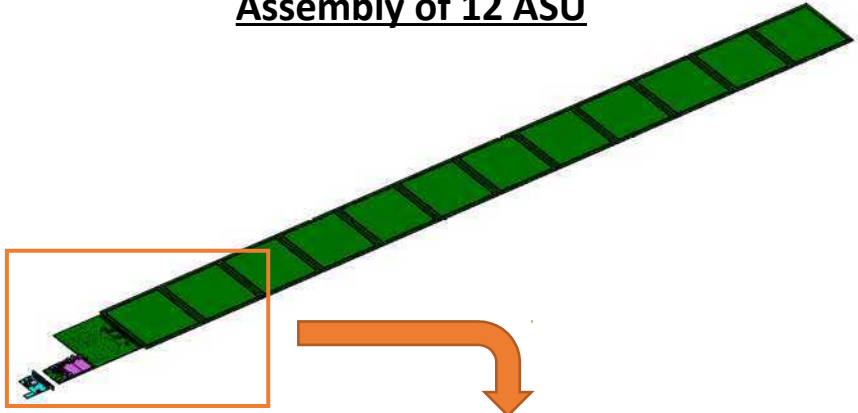
Shielding

- vs Light and CEM

⇒ Power & Signal Integrity



Assembly of 12 ASU



Mechanical Assembly for SLABs

Fragile Wafers

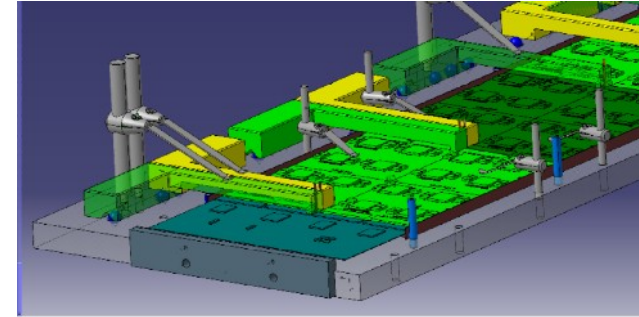
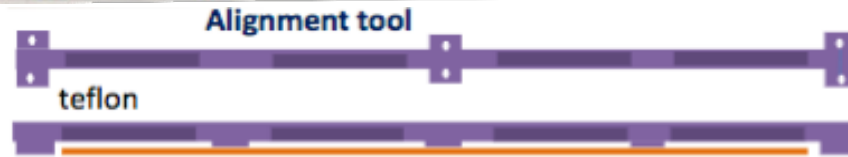
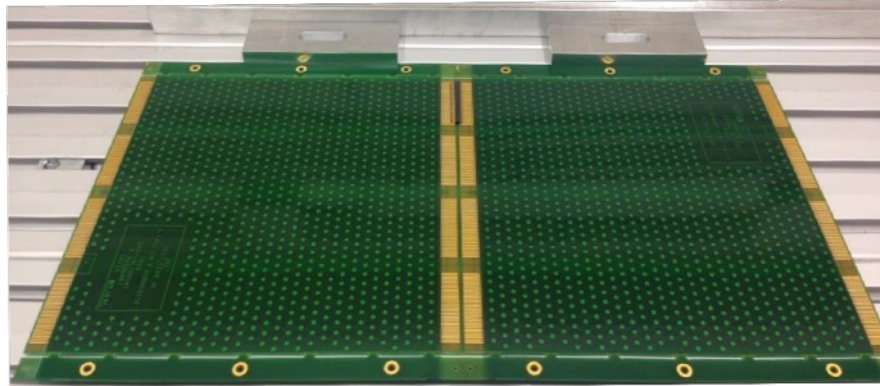
Medium precision of PCB's

⇒ Assembly benches

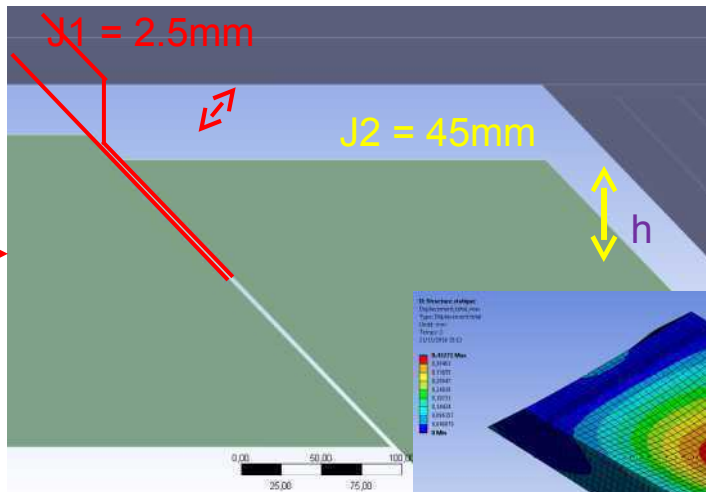
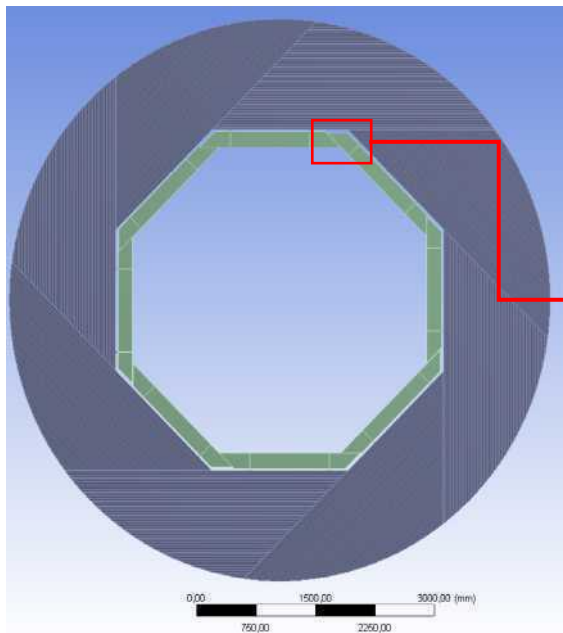
Connections to be handled
by industry

- Dedicated Kaptons ✗
- → connectors

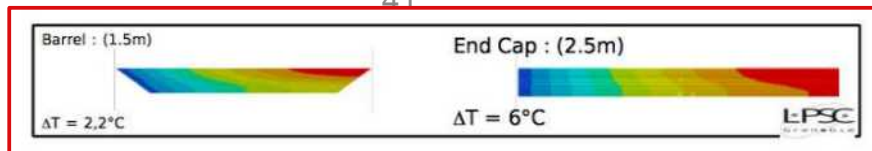
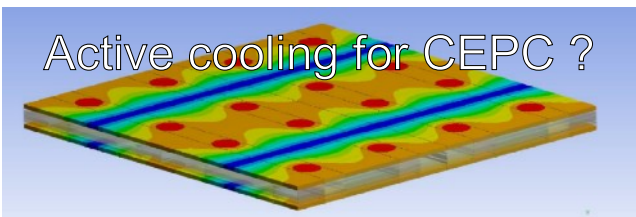
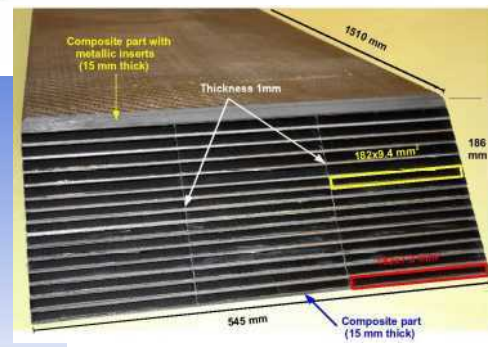
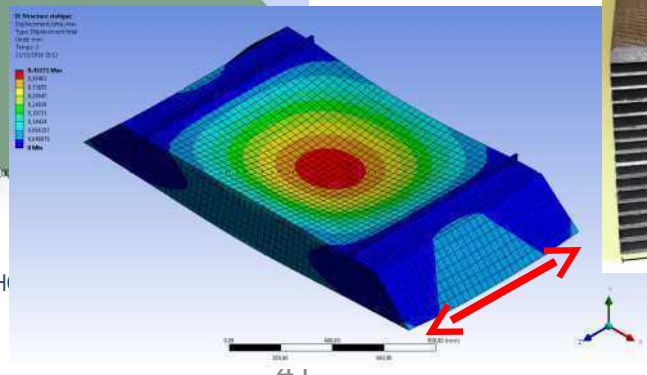
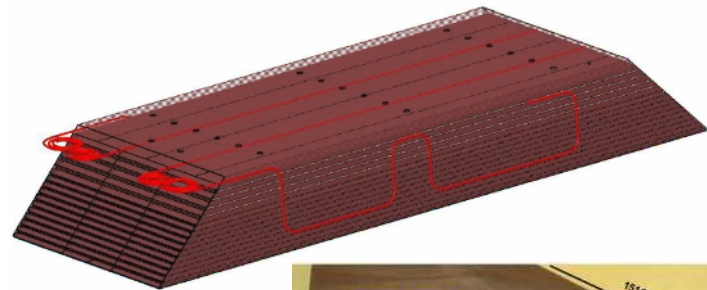
Embedded ultra-thin super-capacitors



Thermo-mechanical simulations



J1 = clearance between modules for the ECAL
 J2 = Clearance at ECAL edges between ECAL and H
 h = height of the rails 30mm

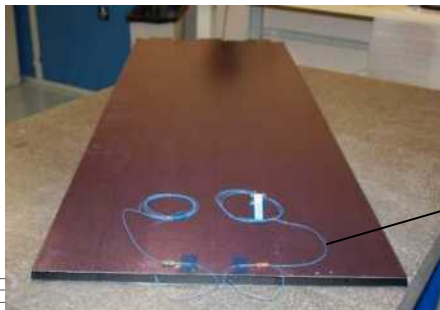


H. Videau, M. Anduze, T. Pierre-Émile
 + LPSG (D. Grondin, J. Giraud)

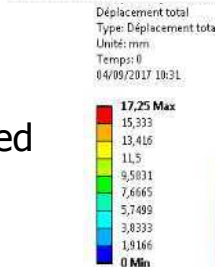
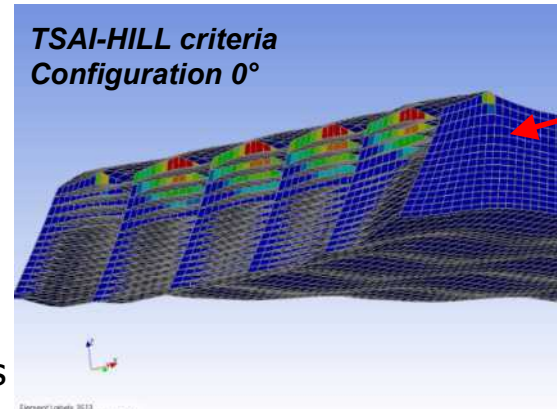
Mechanical simulations



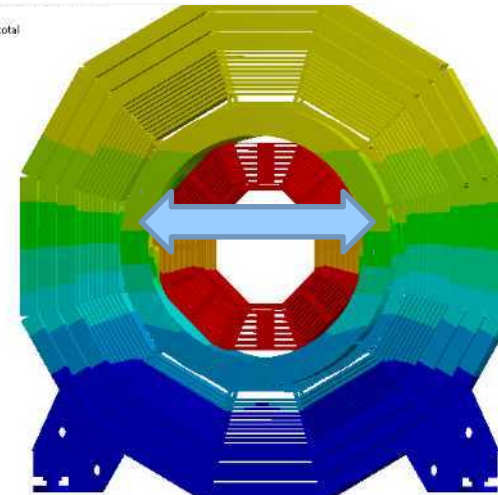
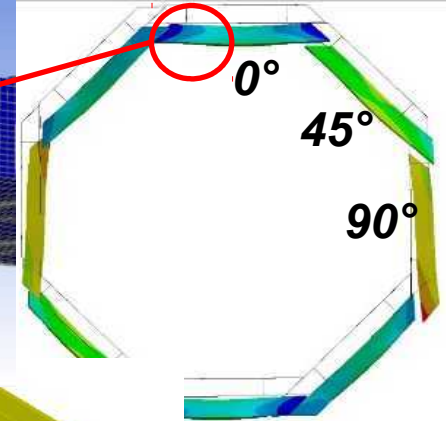
- All dimensions of the ILD prototype are defined according to FEA results in **static and dynamic (earthquake) conditions** and for all positions of final modules in the barrel (8 cases)
- Study of deformations and **limit stresses** analysis using composite criteria (TSAI-HILL)
Max stresses are located on the top ribs, a strong effort is needed to define correctly its thickness
- Proposal: Study internal stresses by using new sensors : **optical fiber Bragg grating sensors** embedded directly within ribs (strain gauge behaviour)



Optical fiber equipped with BG sensors



Global deformation of ECAL module (static case)



Global déformation (Response spectrum in lateral only)

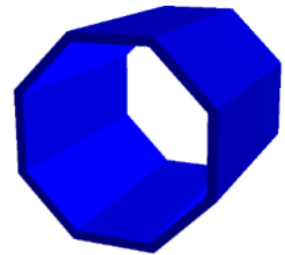
Simulation



ECAL driver used in ILD models has been largely re-written (Mokka → DD4HEP)

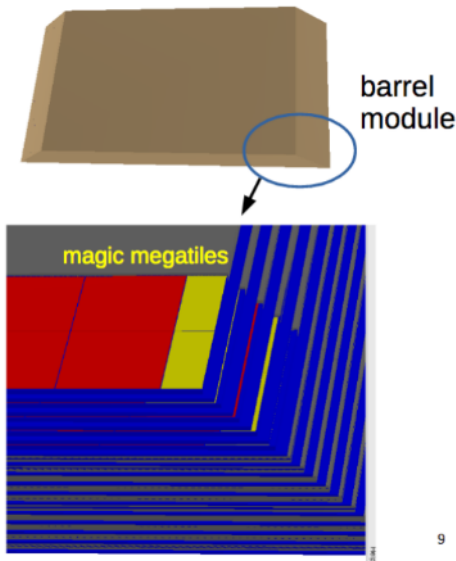
- more modular code:
- less duplication Barrel & Endcap
- more configurable...

ECAL barrel



standard megatiles

layers inside module

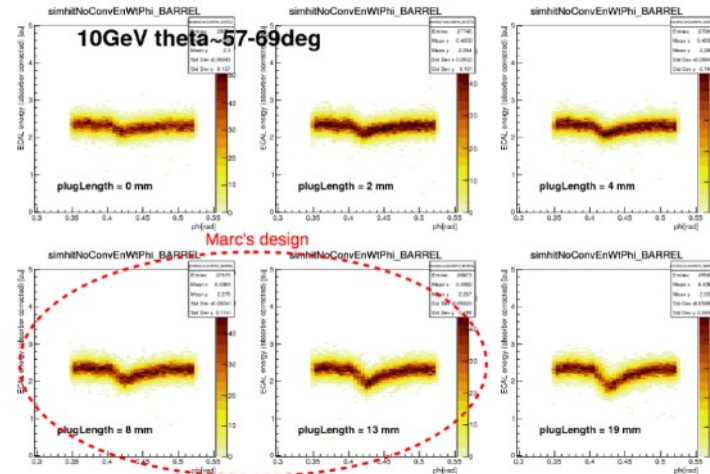
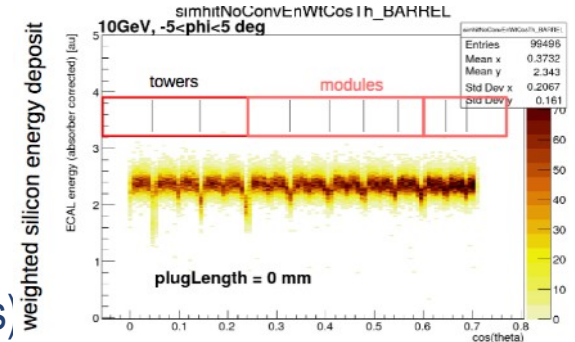


barrel module

Effect of cracks [RAW= no correction at all!!]

– Drop ~ 15%

Effect of plug (missing in previous simulations)



ECAL Services & Cables (Baseline)

Realistic detector proposal

Power, cables and cooling would run between HCAL and ECAL on the back of ECAL (the way it is shown in the picture which exhibits the principle rather than any real design)

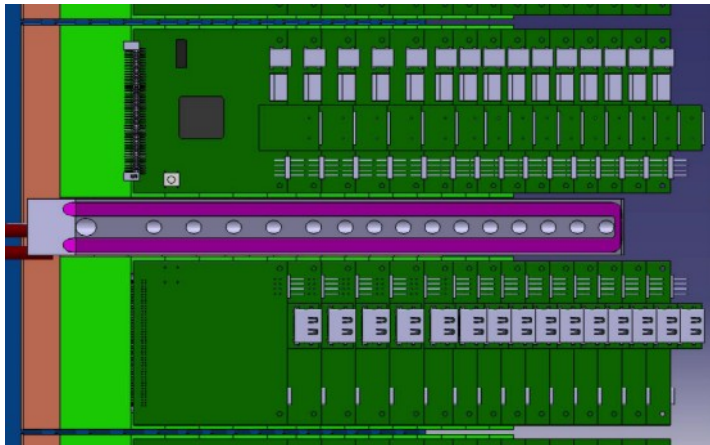
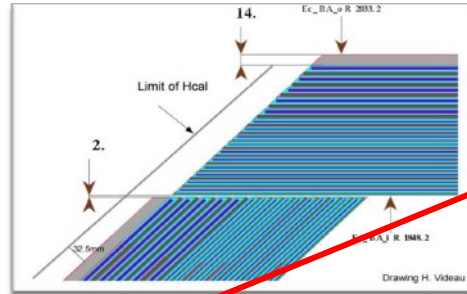
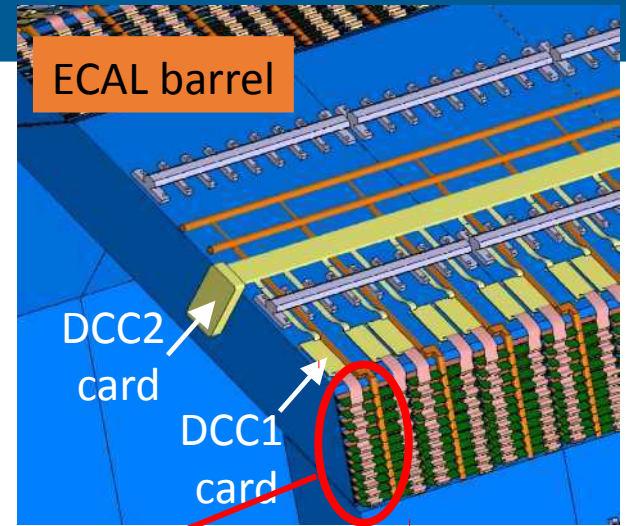
The paths of cables and cooling interfere strongly (cross).

As a working assumption the cables would run to one end of the staves and the cooling to the other end.

- DCC1 figures a concentration/distribution at the alveoli level
- DCC2 (or Hub2) a concentration/distribution at the stave level.

From then cables or fibres run along each sub-detectors to the outside

Same principle will apply for End cap cooling and cables

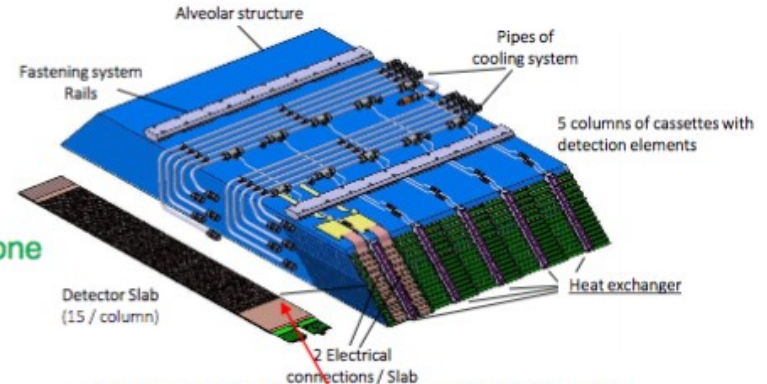


Cooling

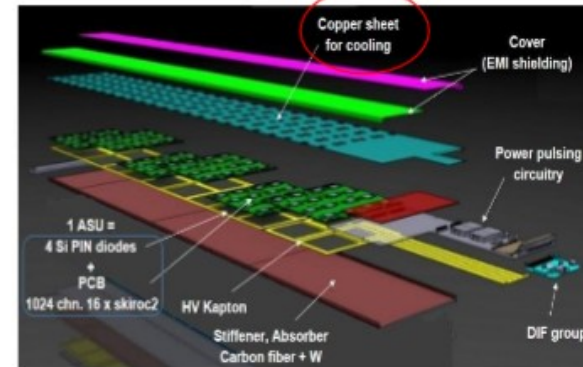
ECAL: (CFRP+W structures + Silicon detectors)

The cooling technology is active, using fluid circulation

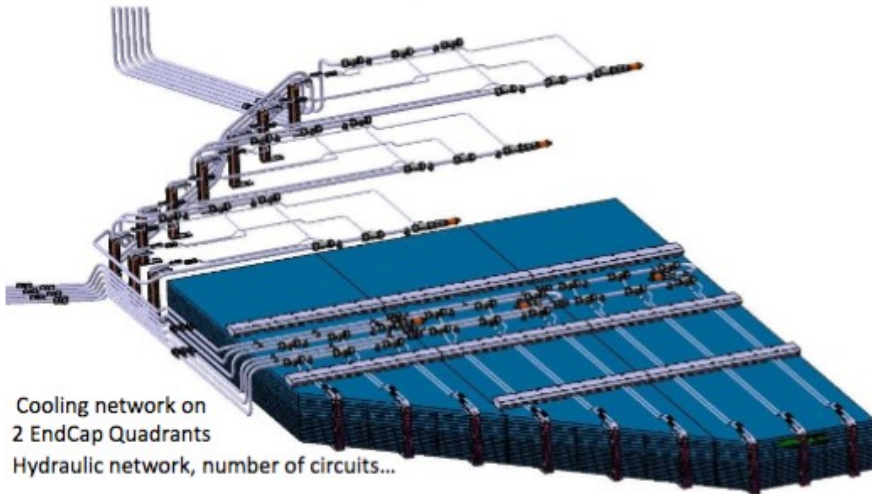
- **Tests and simulation on detector (EUDET module)**
 - Demonstration and performance of Thermal model → done
- **Integration**
 - Detailed design of cooling pipes scalable to ECAL detector → done
- **Thermal model**
 - Full Leakless System Design and Analysis: update in for estimation of global pressure drops → done



Schematic view of 1 ECAL barrel alveolar module with its cooling system - 10 to 15 layers of double sided integrated detector elements (SLABs) in a Tungsten-Carbon Fiber (W-CF) support



Exploded view of half a long slab with 6 ASU – (An assembly line for long slabs with 8 connected ASU is AIDA-2020 deliverable D14.3) final goal with power pulsing 1/100 s: ECAL 4.6 Kw



Full Leakless Design, Grandin | AIDA 2020 | WP4.4 - ECE Meeting | Issues 415 | 2018 | Page 5 / 16

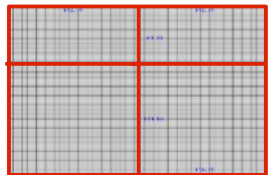
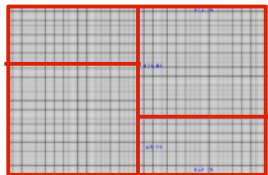
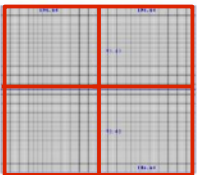
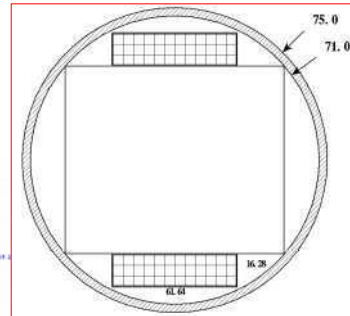
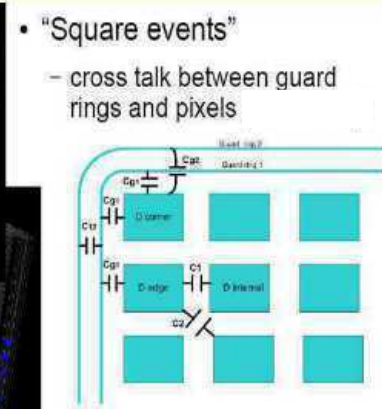
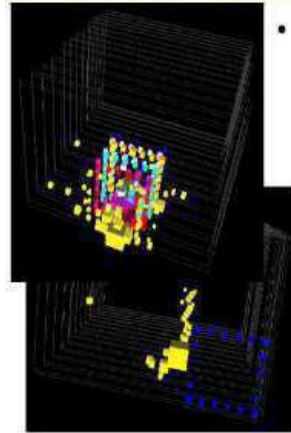
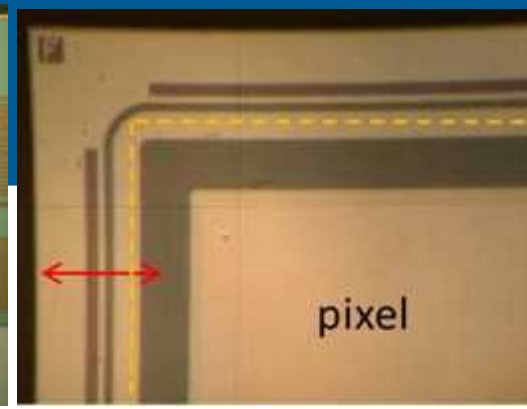
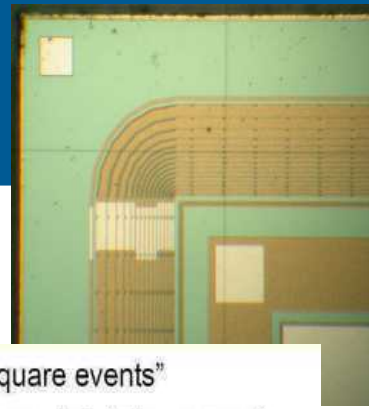
Silicon Sensors

Cost driven

- ~30% of the total cost of the SiW-ECAL
 - ⇒ Units Cost reduction(CALIIMAX program)
- Decoupling of Guard Ring (Square Events).
- new design of ILD detector

Command Sensors (@ Hamamatsu)

- ⚠ Minimal cost of Command $\geq 20k\text{€}$
- direct contact with HPK engineers
- Possibility of design for 8" in 186mm alveola



'quantum unit' of ILD dimensions (here 6" wafer)

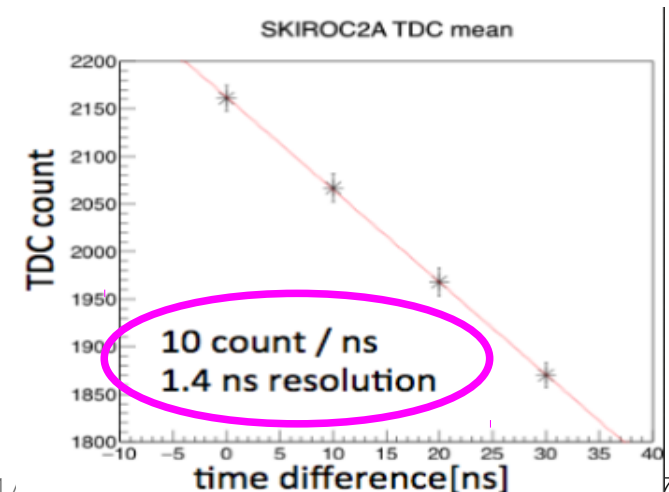
Test of SK2A → Timing ?

Adding 5th dimension:

- **Can:**
 - Improve Particle Flow SW *with ~ns mip precision*
 - Tracking of particles
 - Removal of late neutrons
 - Identification of back scattered
 - Allow Particle identification by ToF *with sub-ns precision*
- *Clean Clock distribution*
 - Shower timing $\sim 1/\sqrt{E}$
- @ LHC See presentation on HGCAL

Checked SK2A on Test Board

- Thorough checks on 1–2 mip injected signal
 - All seems OK
 - No difference in Analog part
- **Trigger:**
 - large channel-by-channel adjustment ✓
 - TDC: OK



SiD SiW-ECAL

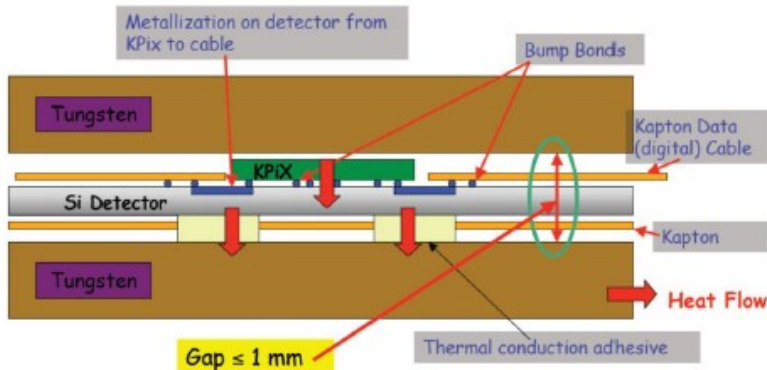


20 + 10 layers

1.25 mm gap between W layers

- Minimize R_M (~ 13 mm effective)
- Keep calorimeter compact

Tungsten plates \Rightarrow thermal bridge to cooling



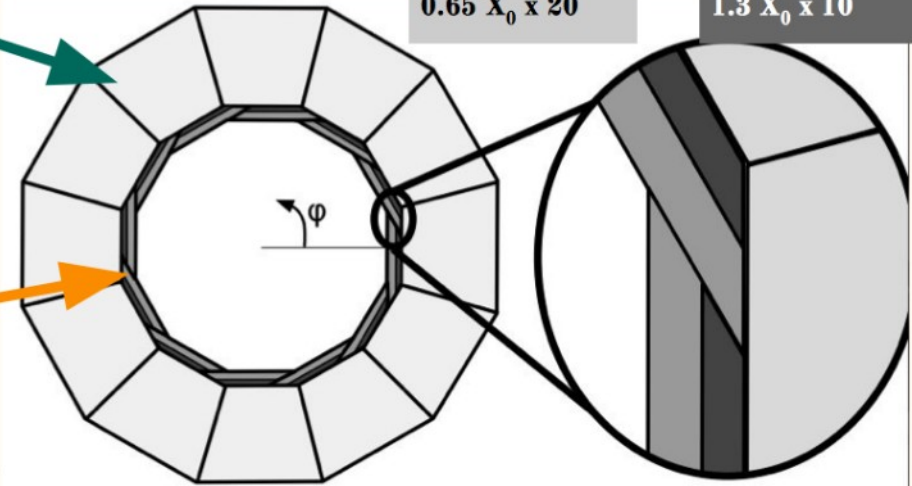
Calorimeter Geometry

HCal

Scintillator sampling calorimeter
Steel/polystyrene

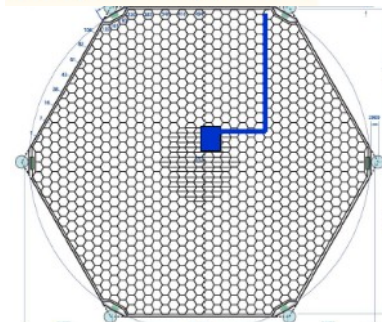
Thin W layers
 $0.65 X_0 \times 20$

Thin W layers
 $1.3 X_0 \times 10$



ECal

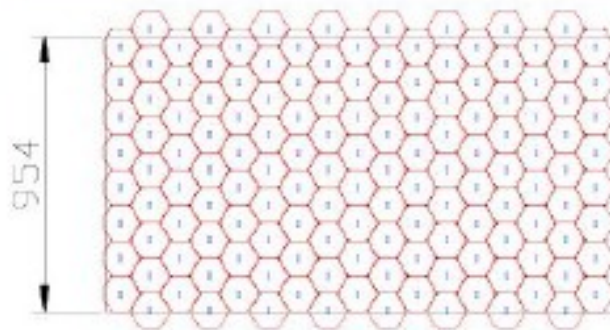
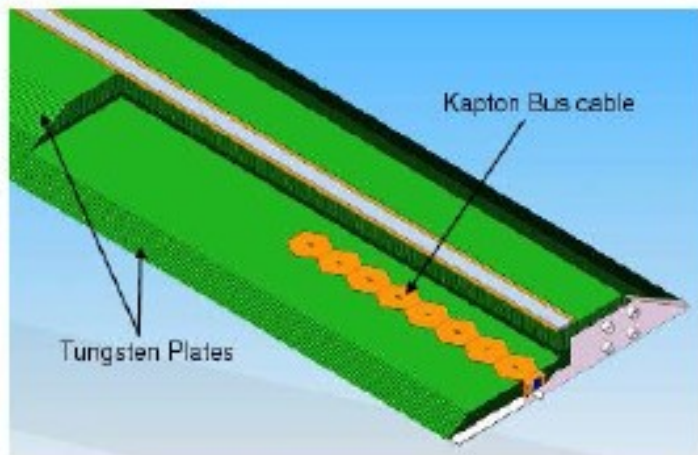
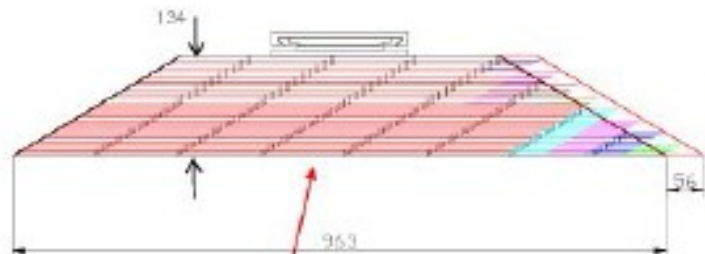
Solid state sampling calorimeter
Tungsten alloy/silicon



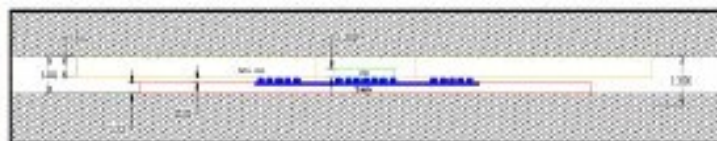
Hexagonal Wafers (optim material)

1 Kpix Chips (1024 ch) per Wafer

- Bump Bounder on Sensor.



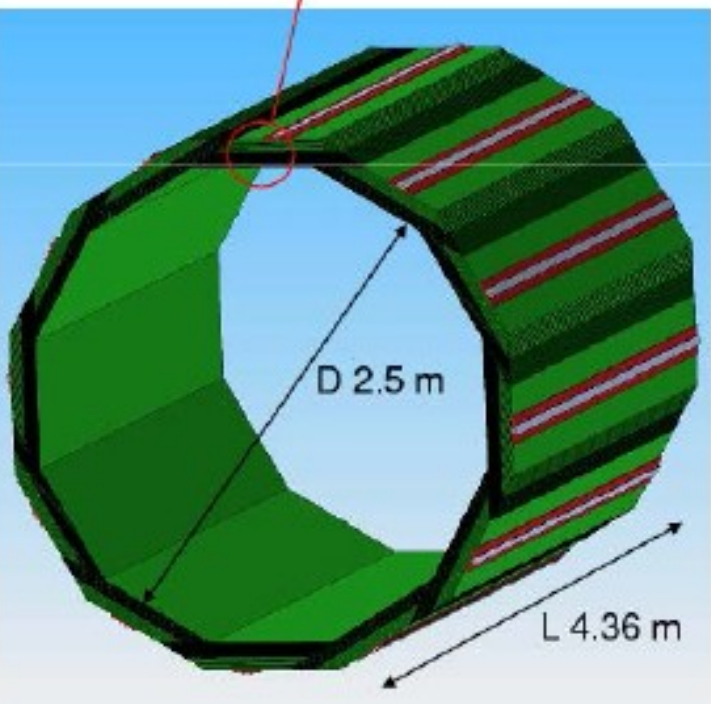
Hexagon sensors arrangement



detector module between tungsten plates

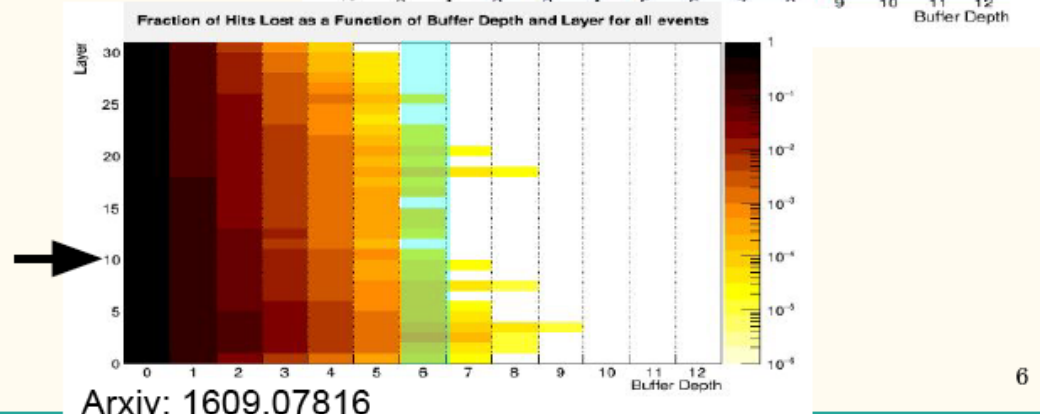
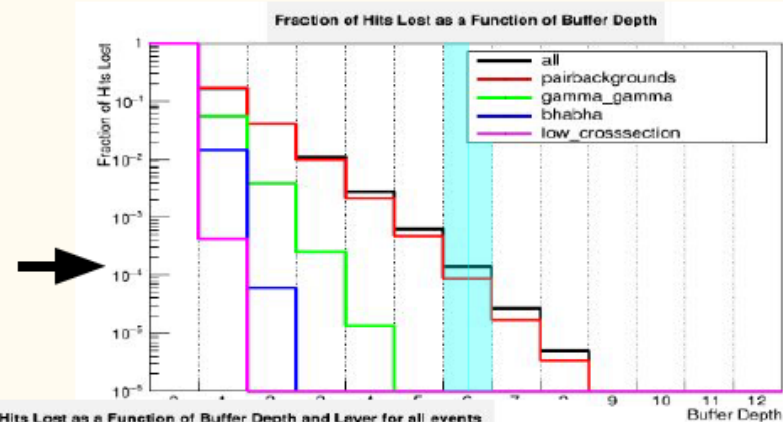
Baseline configuration:

- transverse: 12 mm² pixels
- longitudinal: (20 x 5/7 X₀) + (10 x 10/7 X₀) ⇒ 17%/sqrt(E)
- 1 mm readout gaps ⇒ 13 mm effective Moliere radius



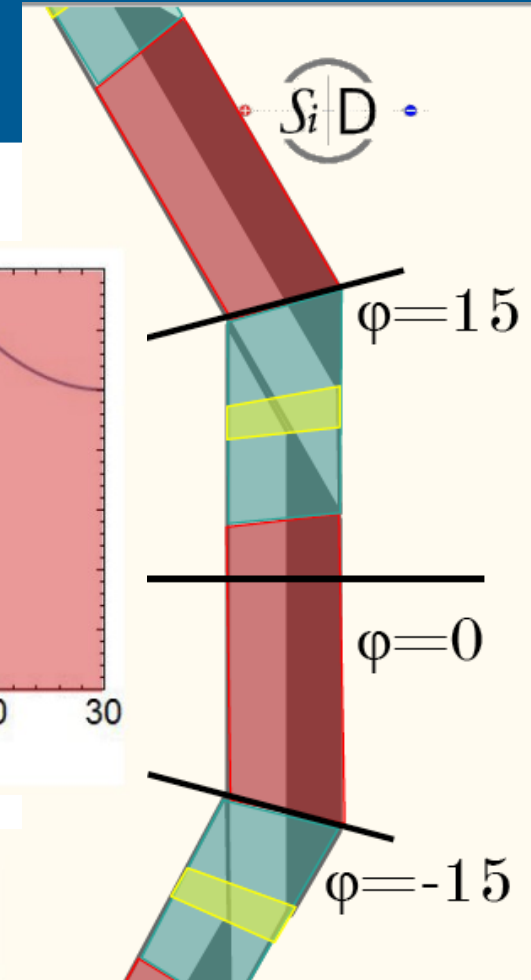
KPiX Studies - Buffer Multiplicity

- Forward multiplicity might be more than 4 buffer KPiX (current design) could handle
 - Recent optimization studies indicate that 6 buffers will be adequate, taking into account all known processes.
- 6 buffers also improve fractional hit loss within detector at shower max and radially
- Must study KPiX to see if more buffers might be added while preserving architecture (preconceptional ideas only)



Arxiv: 1609.07816

Geometry & Calibration studies

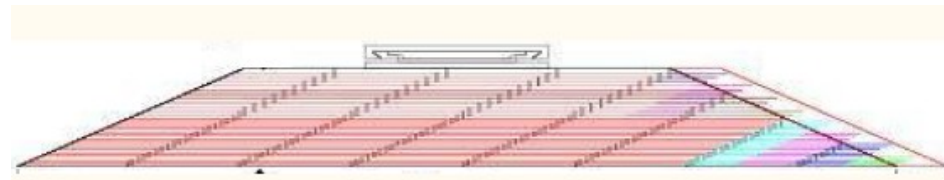
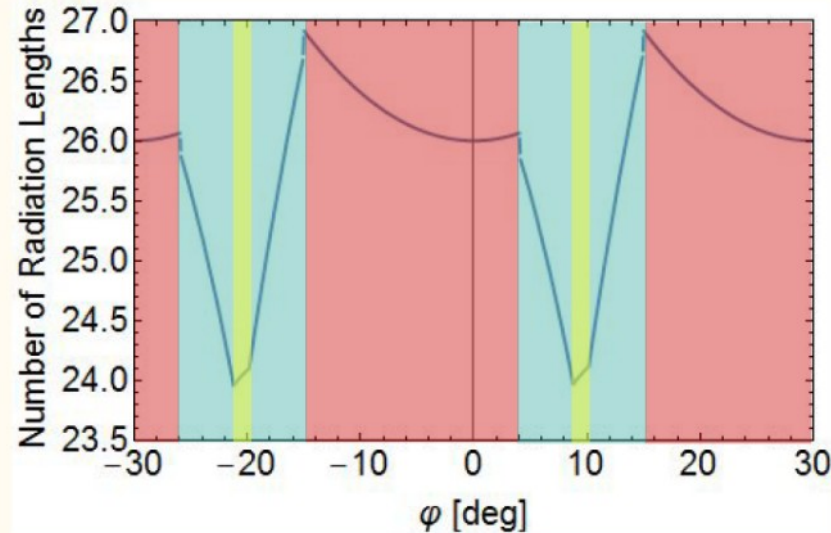


Periodic structure - $\varphi = 30^\circ$ increments

- Entire module,
- overlap region,
- thin overlap region

30% of detector coverage has overlapped modules

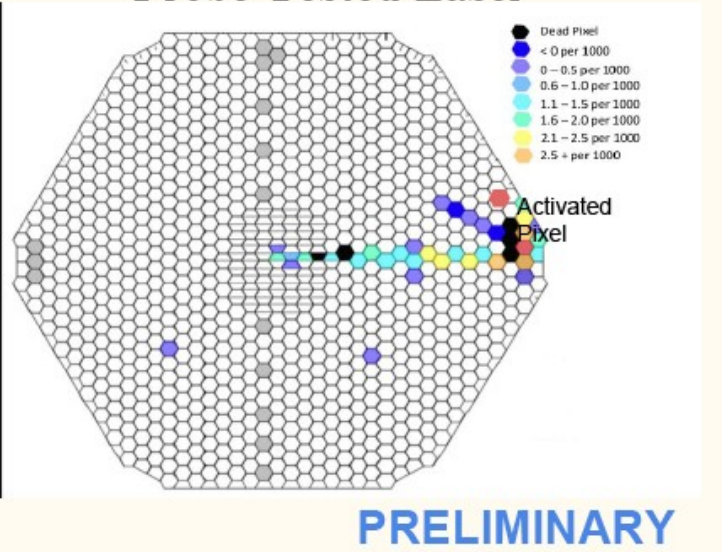
+ leakage corrections



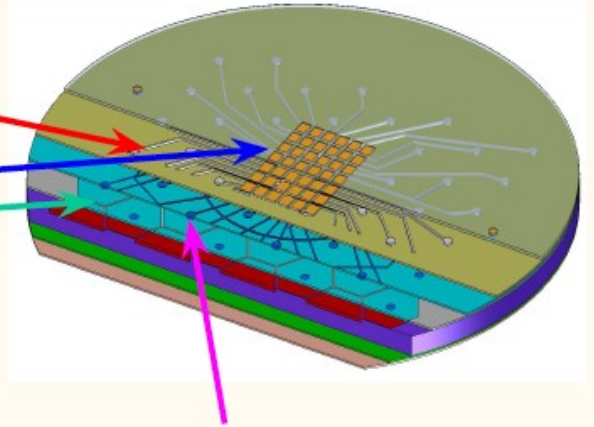
Prototype testing

Laser injection in single pad

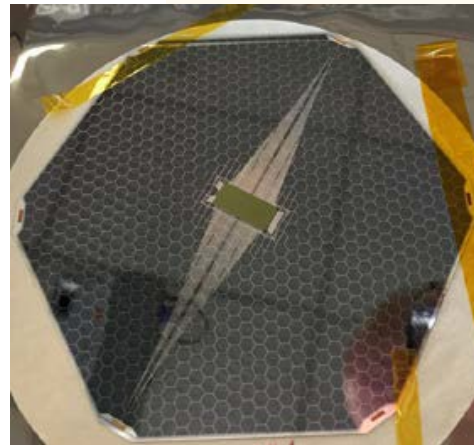
Probe Tested Laser



In present design, **metal 2 traces** from pixels to pad array run over other pixels: parasitic capacitances cause crosstalk.

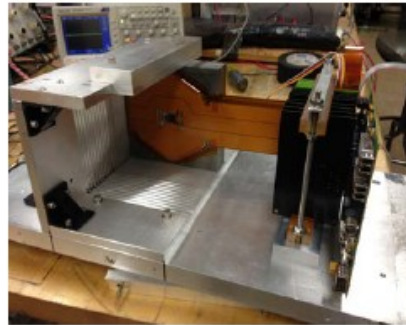
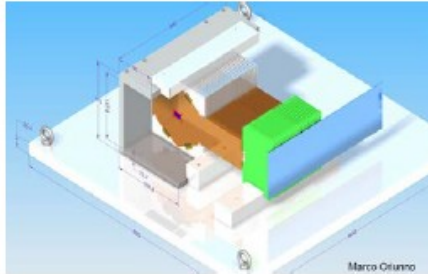


New scheme has “same” metal 2 traces, but a fixed potential metal 1 trace shields the signal traces from the pixels.

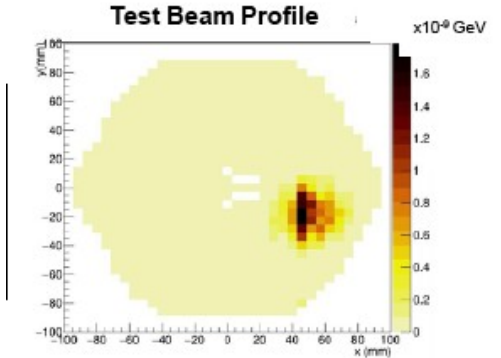


Beam test of 9 layers @ DESY

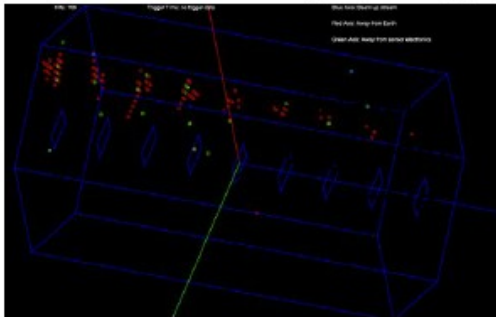
U of Oregon, SLAC, UC Davis



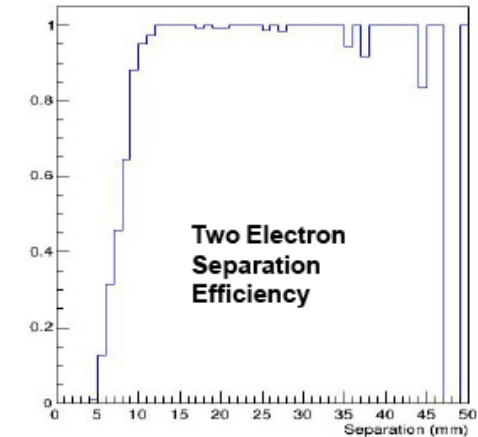
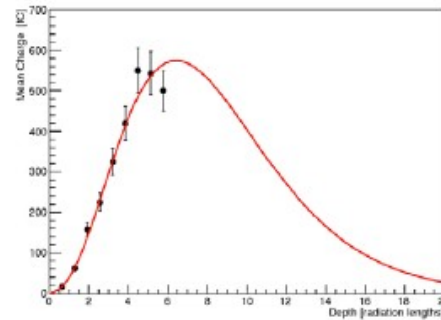
9 layer Si/W
Calorimeter
 $\sim 6 X_0$
13 mm² pixels
12.1 GeV electrons



Single electron in 9-layer prototype



Longitudinal charge deposition



- Parasitic crosstalk – new design has additional shield layer
- Issue with KPix resets causing “monster events” – understood/small change
- Move from aluminum bond pads to gold for next sensors

Prospective for SiW-ECAL's

Very attractive solution

- despite price...
 - ... “almost ready” for real implementation

all Particle Flow Detector Concept for e+e- colliders:

- ILC, CEPC, CLIC
- ≠ running conditions, energies “minor” adaptations:
 - Cooling (Active, Passive)
 - Thickness

Recent addition: timing

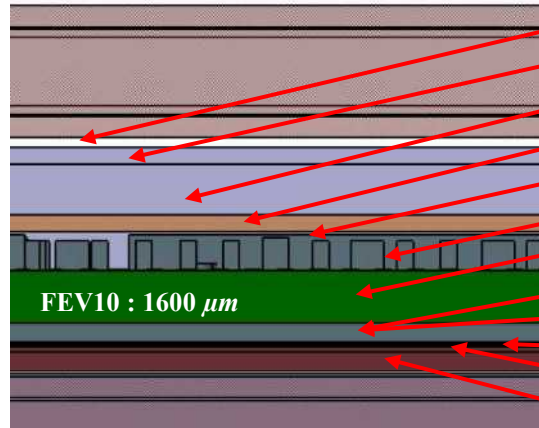
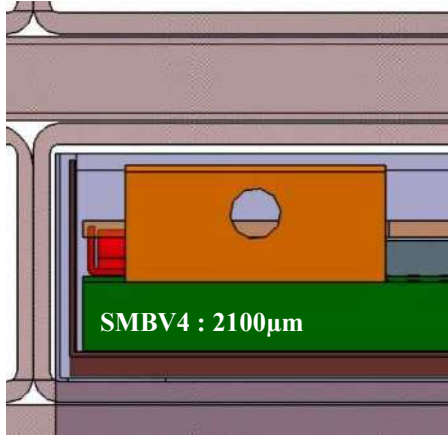
CMS-HGCAL as first “1/2 scale” detector

Extras

3-Sectional view of the BGA slab

3.0 – SLAB THICKNESS

⇒ Conservative design: Short U Slab CIP BGA, height of cell 7,3 mm



- ⇒ Gaps (slab integration) : 410 µm
- ⇒ Shield Cover : 100 µm
- ⇒ Gaps : 1859 µm
- ⇒ Heat shield : 500 µm (LLR)
- ⇒ Pad thermique : 100 µm (LLR)
- ⇒ Chips : 1100 µm
- ⇒ PCB FEV 10 : 1600 µm (flatness ok)
- ⇒ Thickness of glue : 250 µm (LPNHE)
- ⇒ Thickness of wafer : 325 µm (LLR)
- ⇒ Thickness of glue : 150 µm (LAL)
- ⇒ Kapton® film HV : 100 µm (LAL)
- ⇒ Thickness of Carbon : 800 µm (LLR)

ILC Detector Construction Schedule

